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## 2SK2800

# Silicon N Channel MOS FET High Speed Power Switching

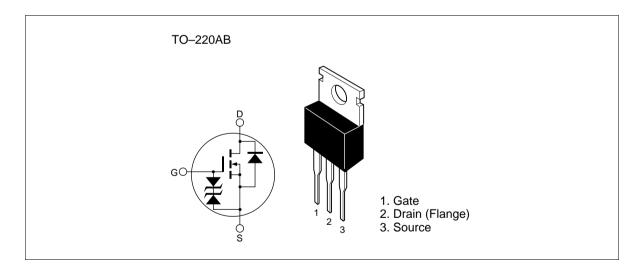


ADE-208-513G (Z) 8th. Edition Jul. 1998

### **Features**

- Low on-resistance  $R_{DS(on)} = 15 \text{ m}\Omega \text{ typ.}$ 
  - High speed switching
- Low drive current
- 4V gate drive device can be driven from 5V source

## **Outline**



## 2SK2800

## **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	40	A
Drain peak current	Note1 D(pulse)	160	A
Body-drain diode reverse drain current	I <sub>DR</sub>	40	A
Avalanche current	I <sub>AP</sub> Note 3	40	A
Avalanche energy	E <sub>AR</sub> Note 3	137	mJ
Channel dissipation	Pch Note 2	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

- Note: 1. PW  $\leq$  10 $\mu$ s, duty cycle  $\leq$  1 %
  - 2. Value at Tc = 25°C
  - 3. Value at Tch =  $25^{\circ}$ C, Rg  $50\Omega$

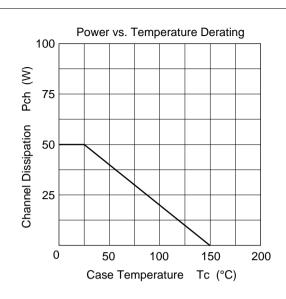
## **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

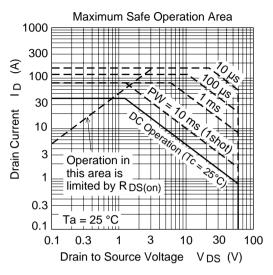
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_{D} = 10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	2.5	V	$I_D = 1$ mA, $V_{DS} = 10$ V
Static drain to source on state	R <sub>DS(on)</sub>	_	15	20	mΩ	$I_D = 20A, V_{GS} = 10V^{Note4}$
resistance	R <sub>DS(on)</sub>	_	25	40	$m\Omega$	$I_D = 20A$ , $V_{GS} = 4V^{Note4}$
Forward transfer admittance	y <sub>fs</sub>	20	35	_	S	$I_D = 20A, V_{DS} = 10V^{Note4}$
Input capacitance	Ciss	_	1500	_	pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	720	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	200	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	20	_	ns	$I_{D} = 20A, R_{L} = 1.5\Omega$
Rise time	t <sub>r</sub>	_	180	_	ns	$V_{GS} = 10V$
Turn-off delay time	t <sub>d(off)</sub>	_	200	_	ns	
Fall time	t <sub>f</sub>	_	200	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.95	_	V	I <sub>F</sub> = 40A, VGS = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	70	_	V	I <sub>F</sub> = 40A, VGS = 0 diF/ dt =50A/µs

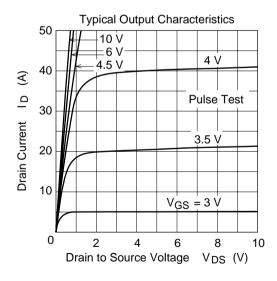
Note: 4. Pulse test

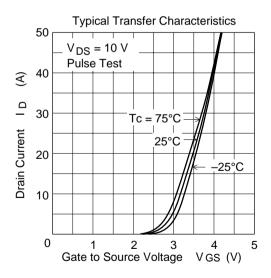
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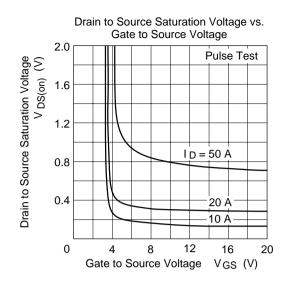
### **Main Characteristics**

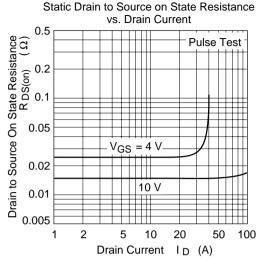


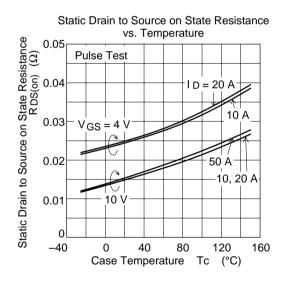


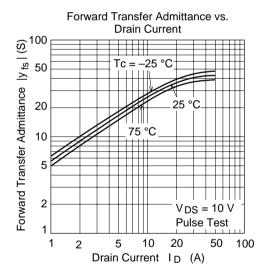


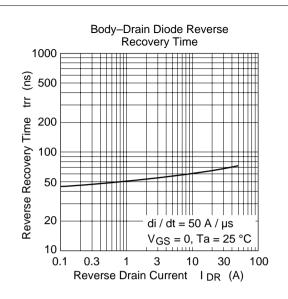


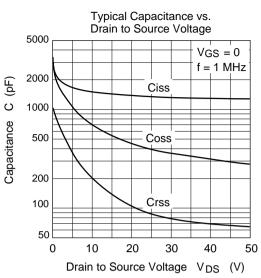


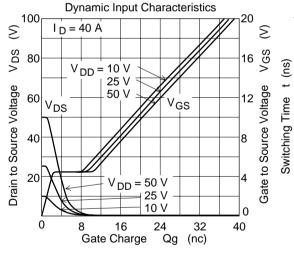


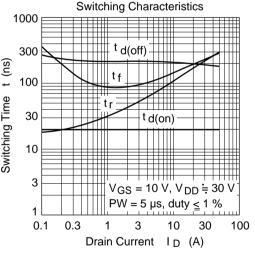


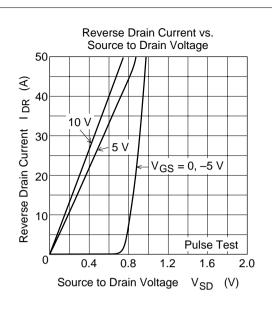


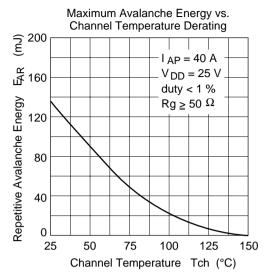


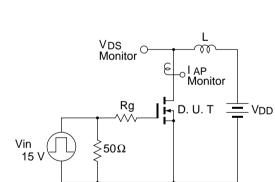




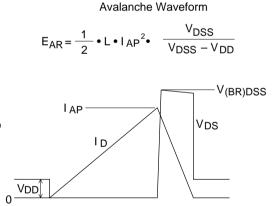


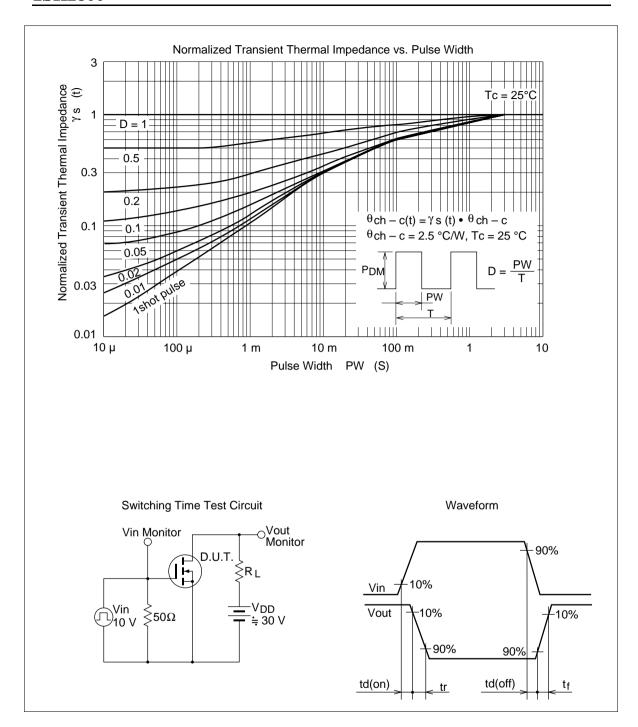




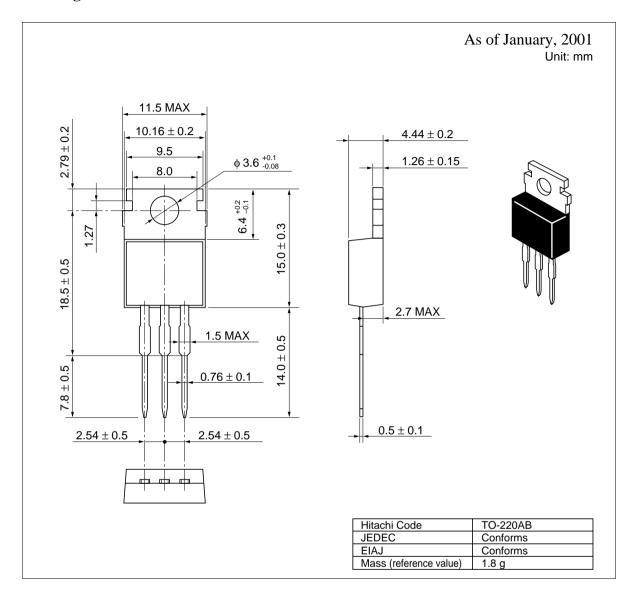


Avalanche Test Circuit





## **Package Dimensions**



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