



PRODUCT CC-DEBUGGER Debugger and Programmer for RF System-on-Chips TI ORIGINAL Free Shipping

SUPPORTED SOFTWARE

SmartRF Studio, SmartRF Flash Programmer, IEEE Address Programmer, PacketSniffer, PurePathWireless, AR for8051 various verisons(including the newest 8.10 used in Z-STACK) ,etc.We Are The Distributor Of TZZT Brand In Hong Kong, China.

SUPPORTED OPERATING SYSTEM

WINXP,WIN7 32/64bit

SUPPORTED CHIPS OVERVIEW

A ful series of CC chips of TI with 8051 inner core, as well as several transceivers,detailed specification as below:

A.chips can be used for programming and simulation:

- △ CC1110,CC1111
- △ CC2430,CC2431
- △ CC2510,CC2511
- △ CC2530,CC2531,CC2533
- △ CC2540,CC2541
- △ CC2543, CC2544,CC2545

B.chips can be contralled through SmartRF Studio:

- △ CC1120,CC1121,CC1125,CC1175
- △ CC1100,CC1101,CC110L,CC113L,CC115L
- △ CC2500,CC2520

C. wireless chips can be used to programme PurePath

△ CC8520,CC8521

△ CC8530,CC8531

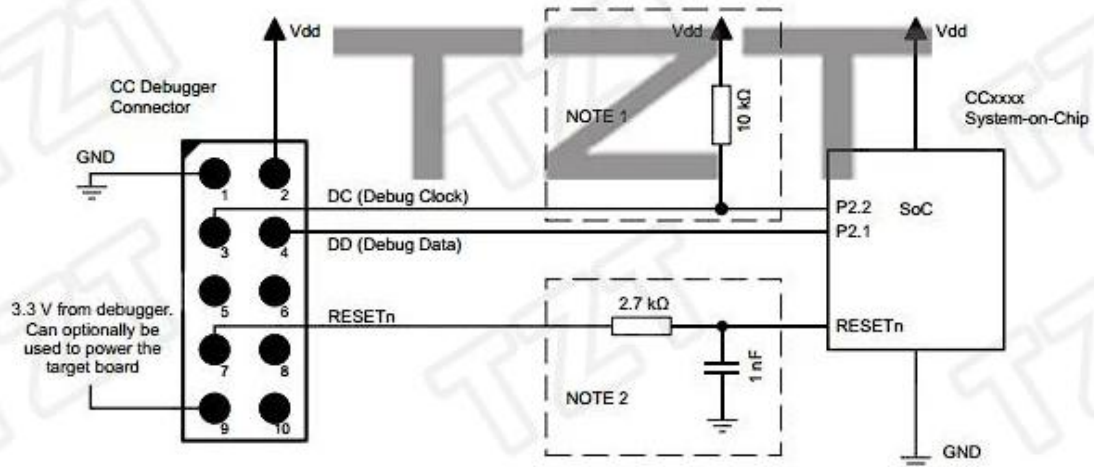


Figure 6. Minimum Connection for Debugging of 8051 SoC

NOTE: Some early revisions of certain SoCs (CC2430, CC2510 and CC1110) needed an external pull-up to avoid unwanted transitions on the debug clock line during chip reset, inadvertently setting the device in debug mode. All new revisions of all SoCs now have an internal pull-up on P2.2, so this external component is not required.

NOTE: The RESETn pin is sensitive to noise and can cause unintended reset of the chip. For reset lines susceptible to noise, it is recommended to add an external RC filter. For recommended RESET circuitry, see the device-specific SoC data sheet and reference designs. The CC Debugger supports slow transitions on the reset line, using a 2 ms delay between any transition on the RESET line and other transitions on the DC and DD lines.

Minimum Connection for SmartRF Packet Sniffer

In order to use the packet sniffer capabilities of the CC Debugger, it is also necessary to connect the SPI bus to the SoC. The SPI interface is used by the CC Debugger for reading the captured RF packets from the SoC (see Figure 7).

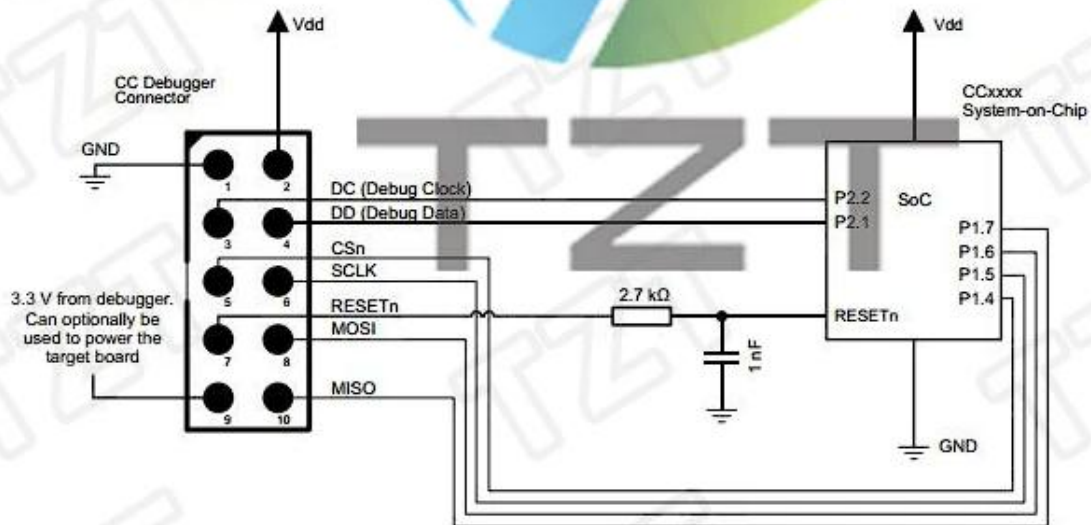


Figure 7. Connection to SoC to Enable Packet Sniffing

Connecting the CC Debugger to a Transceiver

The SPI interface on the CC Debugger can be used to interface many of the CCxxx transceivers and control them from SmartRF Studio. The transceivers, transmitters, and receivers currently supported are:

- CC1100
- CC1101
- CC1120
- CC1121
- CC1125
- CC1175
- CC110L
- CC113L
- CC115L
- CC1200
- CC1201
- CC2500
- CC2520

Note that the CC Debugger operates as the SPI Master. In a multi-master system, it is necessary to make sure the debugger output signals (DC, DD, CSn, SCLK, MOSI and RESETn) do not interfere with the other SPI master on the board. The other SPI master would typically be the microcontroller on the board.

Figure 8 through Figure 10 show the interconnection between the debugger and the various supported transceivers.

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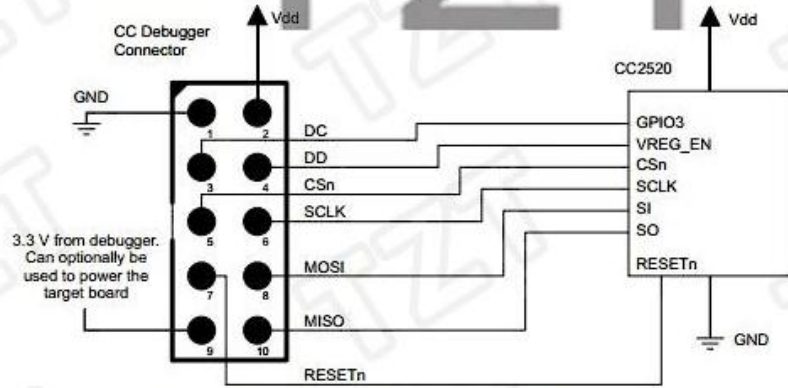
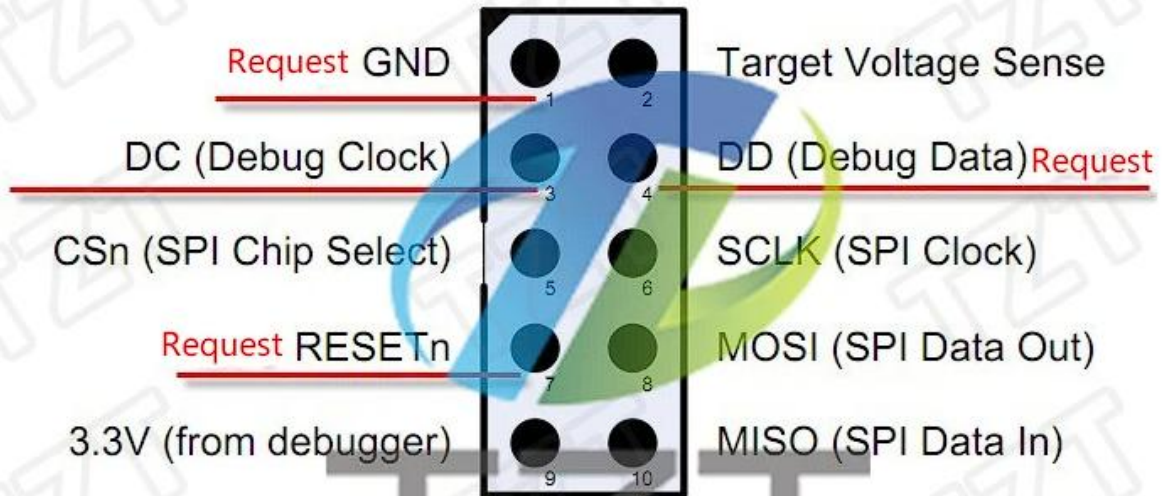
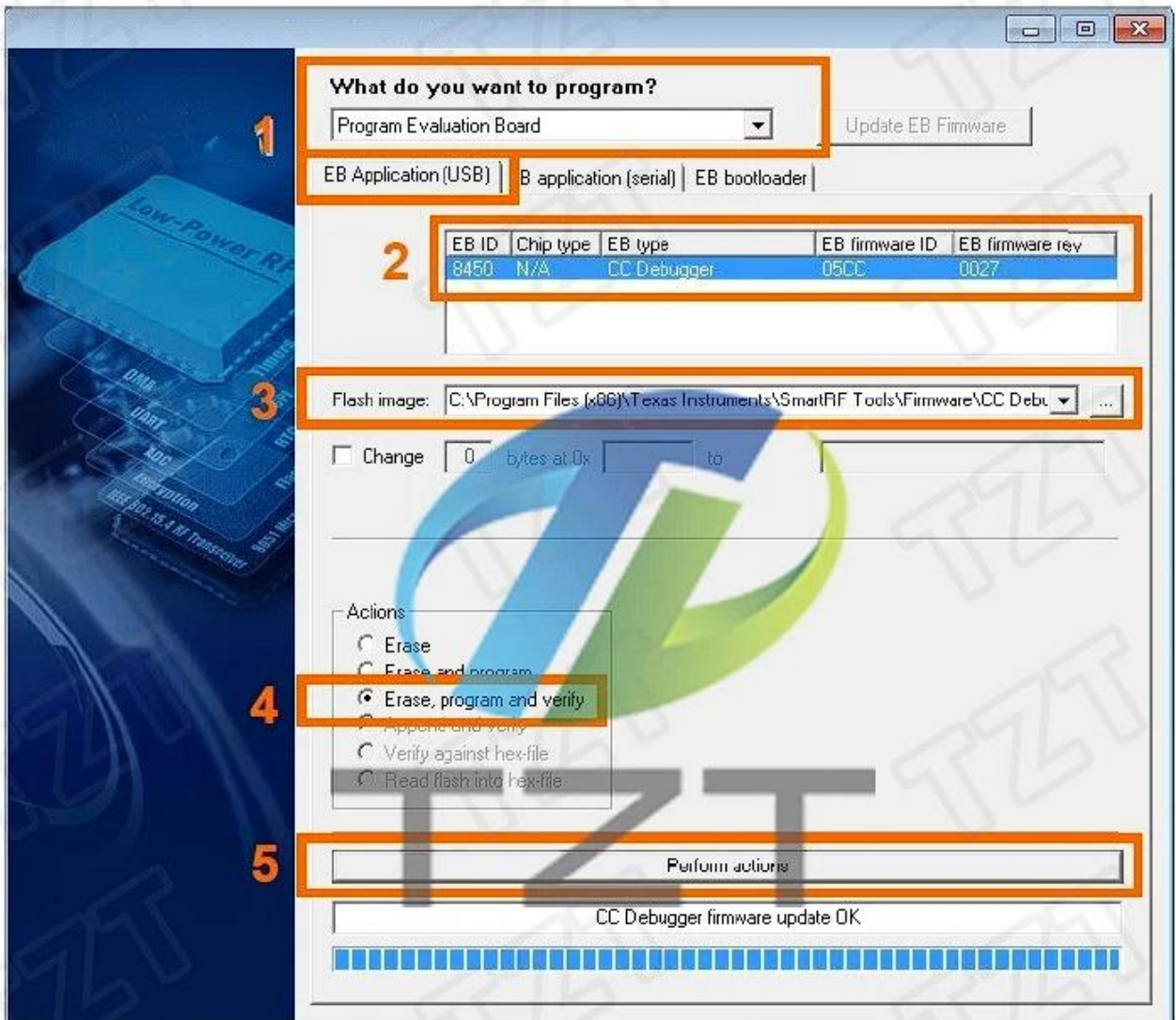
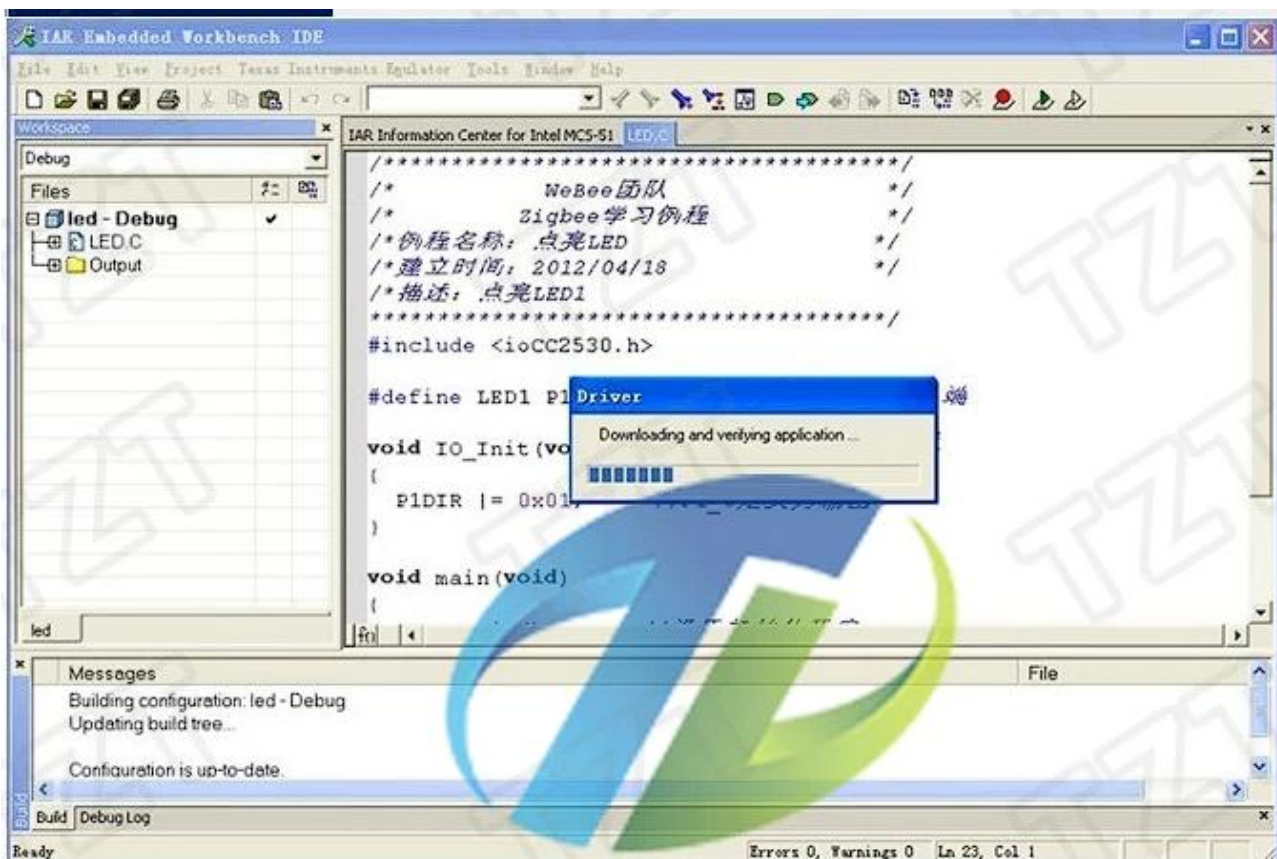


Figure 8. CC Debugger Connected to CC2520



Above 10Pin, the GND/DC/DD/RESET pins must connect to target board , other is choosability. but use for Packet sniffer, other SPI interfaces also must request connect to target board





File Help

ZigBee 2007/PRO

P.nbr.	Time (us)	Length	Frame control field				Sequence number	Dest. PAN	Dest. Address	Source Address	MAC payload		
			Type	Sec	Pnd	Ack.req	PAN	compr					
RX 1	+0 =0	21	DATA	0	0	0	1		0x00	0x2007	0xBEEF	0x2520	00 00 00 00 00 01 02 03 04 05 Type Versio DATA 0x0
RX 2	+10706 =10706	21	DATA	0	0	0	1		0x01	0x2007	0xBEEF	0x2520	01 00 00 00 00 01 02 03 04 05 Type Versio CMD 0x0
RX 3	+10470 =21176	21	DATA	0	0	0	1		0x02	0x2007	0xBEEF	0x2520	02 00 00 00 00 01 02 03 04 05 Type Versio R10 0x0
RX 4	+10471 =31647	21	DATA	0	0	0	1		0x03	0x2007	0xBEEF	0x2520	03 00 00 00 00 01 02 03 04 05 Type Versio R11 0x0

Setup | Select fields | Packet details | Address book | Display filter | Time line |

Select connected device

EB ID: S026, Chip type: CC2530, EB type: CC Debugger

Select packet buffer: 20 MB Select channel: 2400 (2405 MHz) Clock multipl: 1.0

Packet count: 481 Error count: 0 Filter Off

