EN 62368-1



Wide input voltage, non-isolated & regulated single output



FEATURES

- Economical open frame power supply
- High efficiency up to 95%
- No-load input current as low as 0.2mA
- Operating ambient temperature range: -40°C to +85℃
- Output short-circuit protection

K78_JT-500R3-LB series are high efficiency switching regulators. The converters feature high efficiency, low loss and short-circuit protection in a compact SMD package. These products are widely used in applications such as industrial control, instrumentation and electric power.

RoHS

| Selection | Guide | | | | | | |
|---------------|------------------|----------------------|---------|--------------|---------------------|------------|--|
| | | Input Voltage (VDC)* | Output | | Full Load | Capacitive | |
| Certification | Part No. | Nominal | Voltage | Current (mA) | Efficiency (%) Typ. | Load(µF) | |
| | | (Range) | (VDC) | Max. | Vin Min. / Vin Max. | Max. | |
| | K7803JT-500R3-LB | 24 (4.75-36) | 3.3 | 500 | 85/76 | 680 | |
| | K7805JT-500R3-LB | 24 (6.5-36) | 5 | 500 | 90/81 | 680 | |
| EN/BS EN | K78X6JT-500R3-LB | 24 (8-36) | 6.5 | 500 | 91/83 | 680 | |
| EIN/BS EIN | K7809JT-500R3-LB | 24 (12-36) | 9 | 500 | 93/87 | 680 | |
| | K7812JT-500R3-LB | 24 (15-36) | 12 | 500 | 94/88 | 680 | |
| | K7815JT-500R3-LB | 24 (19-36) | 15 | 500 | 95/90 | 680 | |

Note: *For input voltage exceeding 30 VDC, an input electrolytic capacitor of 22uF/50V is required to prevent the module from being damaged by voltage spikes.

| Input Specifications | | | | | | | |
|---------------------------|----------------------|-----------------------|------|------|------|--|--|
| Item | Operating Conditions | Min. | Тур. | Max. | Unit | | |
| No-load Input Current | | | 0.2 | 1.5 | mA | | |
| Reverse Polarity at Input | | Avoid / Not protected | | | | | |
| Input Filter | | Capacitance filter | | | | | |

| Output Specification | IS | | | | | |
|------------------------------|---|-----------------------------------|-------|------|-------------|-------|
| Item | Operating Conditions | Min. | Тур. | Max. | Unit | |
| Voltage Assurger | F H L L L L L L L L L L | 3.3 VDC output | | ±2 | ±4 | ~ |
| Voltage Accuracy | Full load, input voltage range | Others | | ±2 | ±3 | |
| Linear Regulation | gulation Full load, input voltage range | | | ±0.3 | ±0.5 | % |
| Load Regulation | Nominal input voltage, 10% -10 | | ±0.6 | ±1.0 | | |
| Ripple & Noise* | 20MHz bandwidth, nominal | 3.3 VDC output, 30% -100% load | | 50 | 100 | mVp-p |
| | input voltage | Others, 20% -100% load | | 50 | 100 | |
| Temperature Coefficient | Full load | | ±0.02 | | %/ ℃ | |
| Transient Response Deviation | | | ±50 | ±250 | mV | |
| Transient Recovery Time | Nominal input voltage, 25% load step change | | | 0.2 | 1 | ms |

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DC/DC Converter K78_JT-500R3-LB Series

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Short-circuit Protection

Input voltage range

Continuous, self-recovery

Notes: * 1. The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information; 2. With light loads at or below 30%, Ripple & Noise for 3.3V output parts increase to 200mVp-p max, and a load below 20% for 5V/6.5V/9V/12V/15V output parts levels increase to 250mVp-p max.

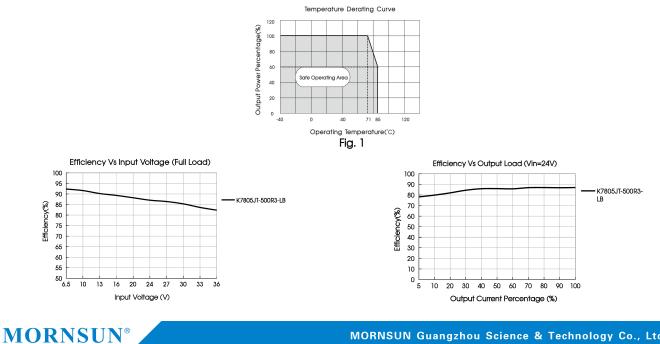
| General Specificatio | ns | | | | |
|--------------------------------------|-------------------------------------|------------|---|------|---------|
| Item | Operating Conditions | Min. | Typ. | Max. | Unit |
| Operating Temperature | See Fig. 1 | -40 | | +85 | °0 |
| Storage Temperature | | -55 | | +125 | °C |
| Storage Humidity | Non-condensing | 5 | | 95 | %RH |
| Reflow Soldering Temperature | | time≤60s o | Peak temp.≤245°C, maximum duration time≤60s over 217°C. For actual application, please refer to IPC/JEDEC J-STD-020D.1. | | |
| Switching Frequency | Full load, nominal input | | 700 | | kHz |
| MTBF | MIL-HDBK-217F@25°C | 2000 | | | k hours |
| Moisture Sensitivity Level (MSL)* | IPC/JEDEC J-STD-020D.1 | | Level 1 | | |
| Note: * For actual application, plea | se refer to IPC/JEDEC J-STD-020D.1. | | | | |

Note: * For actual application, please refer to IPC/JEDEC J-STD-020D.1.

| Mechanical Specifications | | | | |
|---------------------------|---------------------|--|--|--|
| Dimensions | 12 x 12 x 4.5mm | | | |
| Weight | 0.75g (Тур.) | | | |
| Cooling Method | Free air convection | | | |

| Electrom | Electromagnetic Compatibility (EMC) | | | | | | |
|------------|-------------------------------------|------------------|---|------------------|--|--|--|
| Frankriger | CE | CISPR32/EN55032 | CLASS B (see Fig. 4-2) for recommended circuit) | | | | |
| Emissions | RE | CISPR32/EN55032 | CLASS B (see Fig. 4-2) for recommended circuit) | | | | |
| | ESD | IEC/EN 61000-4-2 | Contact ±4kV | perf. Criteria B | | | |
| | RS | IEC/EN 61000-4-3 | 10V/m | perf. Criteria B | | | |
| Immunity | EFT | IEC/EN 61000-4-4 | 100kHz ±1kV (see Fig. 4-① for recommended circuit) | perf. Criteria B | | | |
| | Surge | IEC/EN 61000-4-5 | line to line ±1kV (see Fig. 4- $\ensuremath{\mathbb{1}}$ for recommended circuit) | perf. Criteria B | | | |
| | CS | IEC/EN 61000-4-6 | 3Vr.m.s | perf. Criteria B | | | |

Typical Characteristic Curves

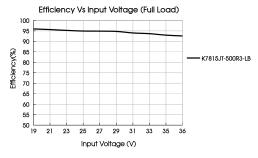


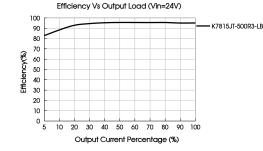
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DC/DC Converter K78_JT-500R3-LB Series

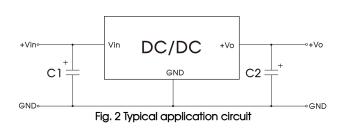
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Design Reference

1. Typical application



| Table 1 | | | | | | |
|------------------|------------------------------|------------------------------|--|--|--|--|
| Part No. | C1 (ceramic capacitor) | C2 (ceramic capacitor) | | | | |
| K7803JT-500R3-LB | | 22µF/10V | | | | |
| K7805JT-500R3-LB | | 22µF/10V | | | | |
| K78X6JT-500R3-LB | | 22µF/16V | | | | |
| K7809JT-500R3-LB | 10µF/50V | 22µF/16V | | | | |
| K7812JT-500R3-LB | | 22µF/25V | | | | |
| K7815JT-500R3-LB | | 22µF/25V | | | | |

Notes:

- 1. The required C1 and C2 capacitors must be connected as close as possible to the terminals of the module;
- 2. Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead;
- 3. Converter cannot be used for hot swap and with output in parallel;
- 4. To further reduce the output ripple and noise, we suggested the use of a "LC" filter at the output terminals, with an inductor value (L) of 10µH-47µH, see Fig. 3

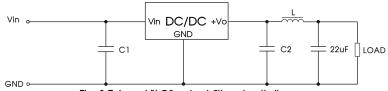


Fig. 3 External "LC" output filter circuit diagram

2. EMC Compliance circuit

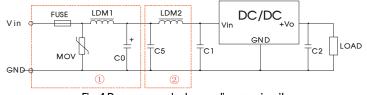


Fig.4 Recommended compliance circuit

| FUSE | MOV | LDM1 | C0 | C1/C2 | C5 | LDM2 |
|---|--------|------|------------|------------------|------------|------|
| Selecting based on the actual input current in application | S20K30 | 82µH | 680µF /50V | Refer to table 1 | 4.7µF /50V | 22µH |

Note: For EMC tests we use Part ${
m (1)}$ in Fig. 4 for immunity and part ${
m (2)}$ for emissions test. Selecting based on needs.

3. For additional information please refer to DC-DC converter application notes on

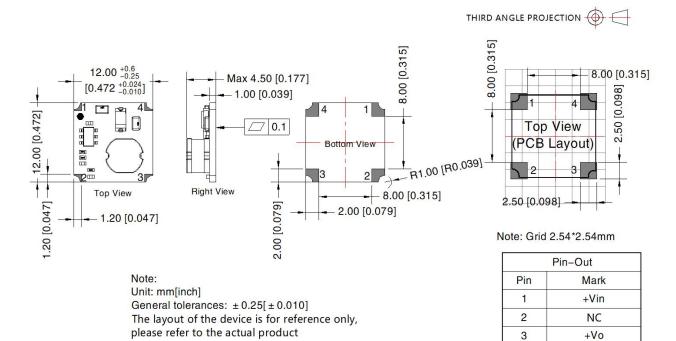
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Dimensions and Recommended Layout





Tape and Reel Info

K78_JT-500R3-LB

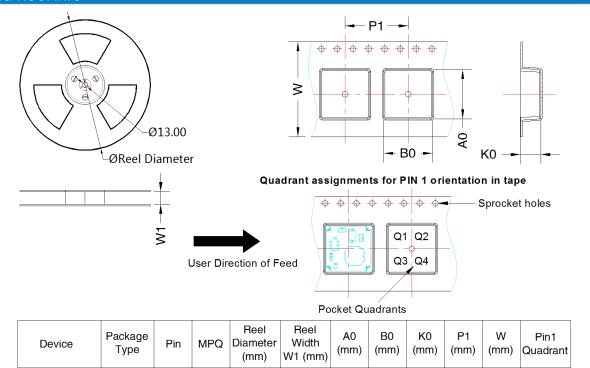
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SMD

4

700

330.0



24.4

12.47

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16

24.0

Q1

4

GND

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5.1



Notes:

- 1. For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58210140;
- 2. The maximum capacitive load offered were tested at nominal input voltage and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datatable are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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