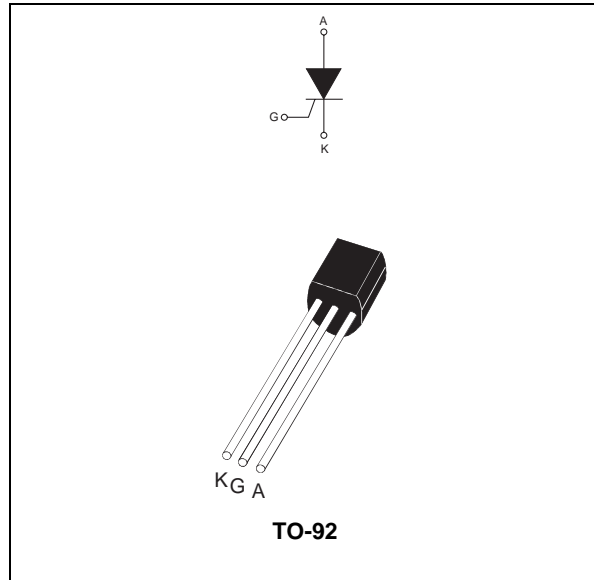


MAIN FEATURES:

| Symbol | Value | Unit |
|-------------------|-------|---------|
| $I_{T(RMS)}$ | 0.8 | A |
| V_{DRM}/V_{RRM} | 600 | V |
| I_{GT} | 200 | μA |

DESCRIPTION

Thanks to highly sensitive triggering levels, the X006 SCR series is suitable for all applications where the available gate current is limited, such as ground fault circuit interrupters, overvoltage crowbar protection in low power supplies, capacitive ignition circuits, ...



ABSOLUTE RATINGS (limiting values)

| Symbol | Parameter | | Value | Unit |
|--------------------|--|------------------------|--------------------------------|------------|
| $I_{T(RMS)}$ | RMS on-state current (180° conduction angle) | | $T_I = 85^\circ C$ 0.8 | A |
| $I_{T(AV)}$ | Average on-state current (180° conduction angle) | | $T_I = 85^\circ C$ 0.5 | A |
| I_{TSM} | Non repetitive surge peak on-state current | $t_p = 8.3 \text{ ms}$ | $T_j = 25^\circ C$ 10 | A |
| | | $t_p = 10 \text{ ms}$ | | |
| I^2t | I^2t Value for fusing | $t_p = 10 \text{ ms}$ | $T_j = 25^\circ C$ 0.25 | A^2S |
| di/dt | Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \leq 100 \text{ ns}$ | $F = 60 \text{ Hz}$ | $T_j = 125^\circ C$ 50 | $A/\mu s$ |
| I_{GM} | Peak gate current | $t_p = 20 \mu s$ | $T_j = 125^\circ C$ 1 | A |
| $P_{G(AV)}$ | Average gate power dissipation | | $T_j = 125^\circ C$ 0.1 | W |
| T_{stg} T_j | Storage junction temperature range Operating junction temperature range | | - 40 to + 125 - 40 to + 125 | $^\circ C$ |

X00602MA

ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise specified)

| Symbol | Test Conditions | | X00602MA | Unit |
|--------------------------------------|---|------------------------|----------|------|
| I _{GT} | V _D = 12 V R _L = 140 Ω | MIN. | 15 | μA |
| | | MAX. | 200 | μA |
| V _{GT} | | MAX. | 0.8 | V |
| V _{GD} | V _D = V _{DRM} R _L = 3.3 kΩ R _{GK} = 1 kΩ T _j = 125°C | MIN. | 0.2 | V |
| V _{RG} | I _{RG} = 10 μA | MIN. | 5 | V |
| I _H | I _T = 50 mA R _{GK} = 1 kΩ | MAX. | 5 | mA |
| I _L | I _G = 1 mA R _{GK} = 1 kΩ | MAX. | 6 | mA |
| dV/dt | V _D = 67 % V _{DRM} R _{GK} = 1 kΩ T _j = 125°C | MIN. | 25 | V/μs |
| V _{TM} | I _{TM} = 1 A t _p = 380 μs T _j = 25°C | MAX. | 1.35 | V |
| V _{th0} | Threshold voltage T _j = 125°C | MAX. | 0.85 | V |
| R _d | Dynamic resistance T _j = 125°C | MAX. | 245 | mΩ |
| I _{DRM} I _{RRM} | V _{DRM} = V _{RRM} R _{GK} = 1 kΩ | T _j = 25°C | MAX. | μA |
| | | T _j = 125°C | | |
| | | | 1 | |
| | | | 100 | |

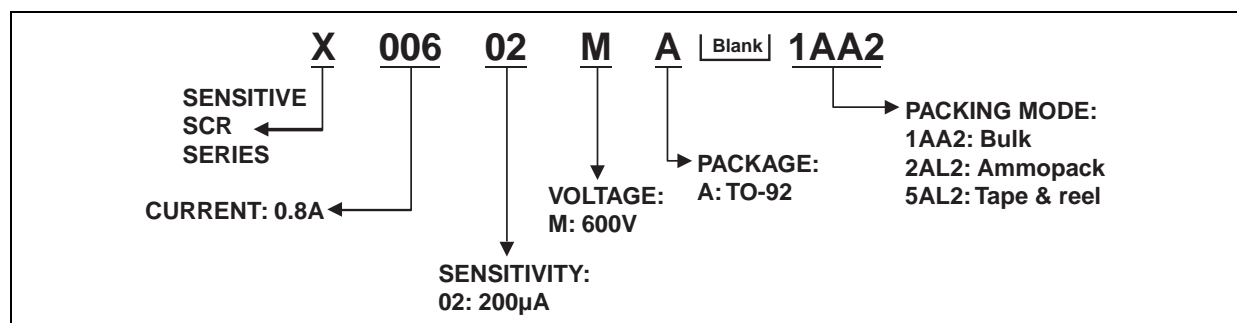
THERMAL RESISTANCES

| Symbol | Parameter | Value | Unit |
|----------------------|--------------------------|-------|------|
| R _{th(j-l)} | Junction to lead (DC) | 70 | °C/W |
| R _{th(j-a)} | Junction to ambient (DC) | 150 | °C/W |

PRODUCT SELECTOR

| Part Number | Voltage | Sensitivity | Package |
|-------------|---------|-------------|---------|
| X00602MA | 600 V | 200 μA | TO-92 |

ORDERING INFORMATION



OTHER INFORMATION

| Part Number | Marking | Weight | Base Quantity | Packing mode |
|---------------|---------|--------|---------------|--------------|
| X00602MA 1AA2 | X0602MA | 0.2 g | 2500 | Bulk |
| X00602MA 2AL2 | X0602MA | 0.2 g | 2000 | Ammopack |
| X00602MA 5AL2 | X0602MA | 0.2 g | 2000 | Tape & reel |

Fig. 1: Maximum average power dissipation versus average on-state current.

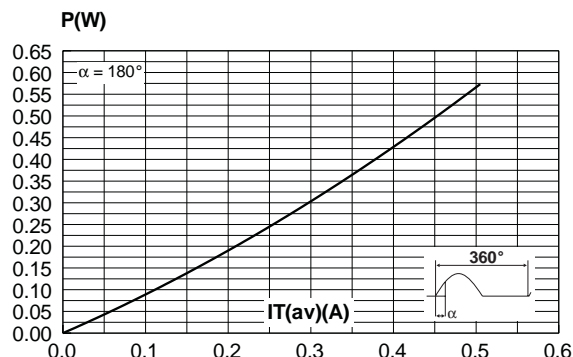


Fig. 2-1: Average and D.C. on-state current versus lead temperature.

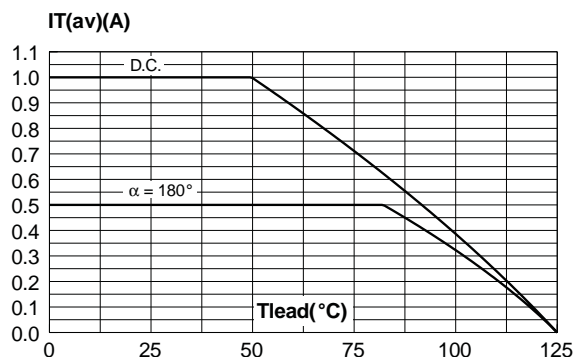


Fig. 2-2: Average and D.C. on-state current versus ambient temperature (device mounted on FR4 with recommended pad layout).

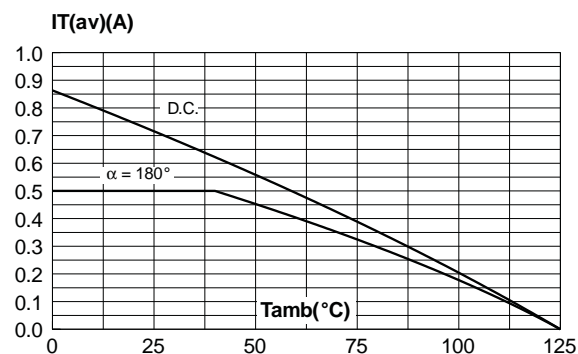


Fig. 3: Relative variation of thermal impedance junction to ambient versus pulse duration.

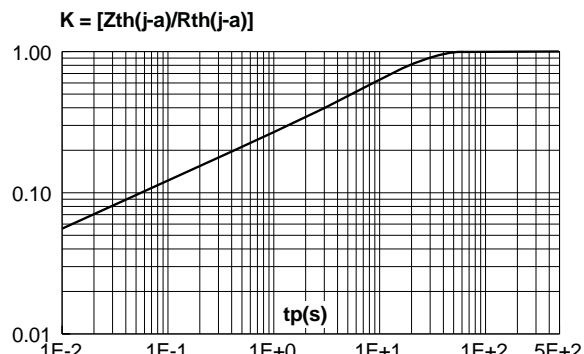


Fig. 4: Relative variation of gate trigger current, holding current and latching current versus junction temperature.

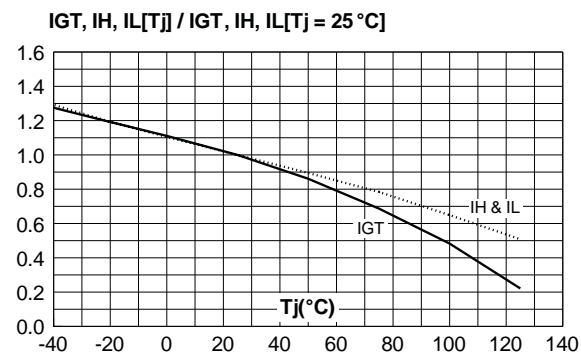


Fig. 5: Relative variation of holding current versus gate-cathode resistance (typical values).

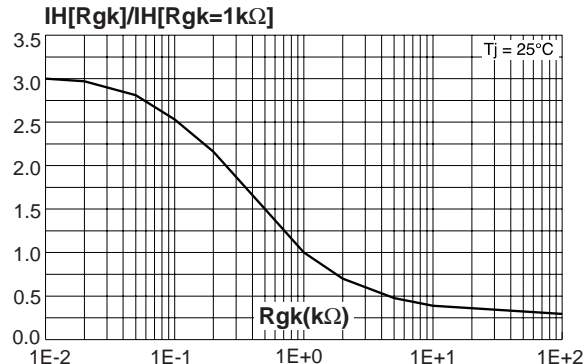


Fig. 6: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).

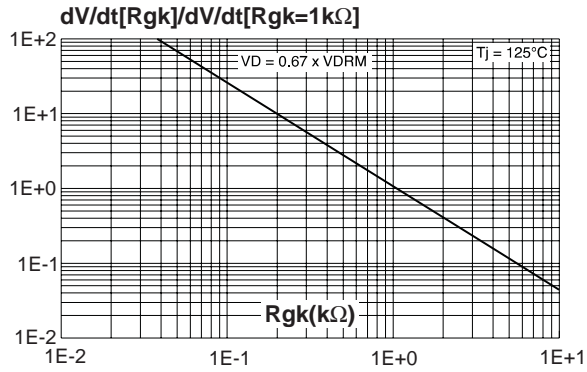


Fig. 8: Surge peak on-state current versus number of cycles.

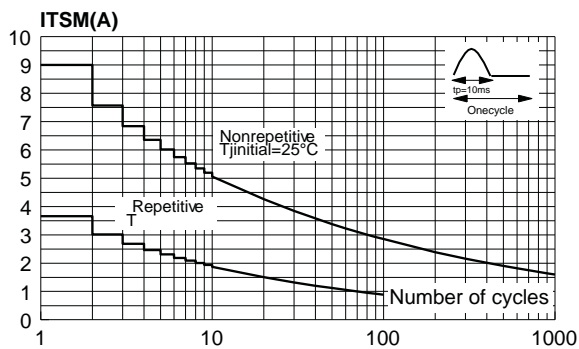


Fig. 10: On-state characteristics (maximum values).

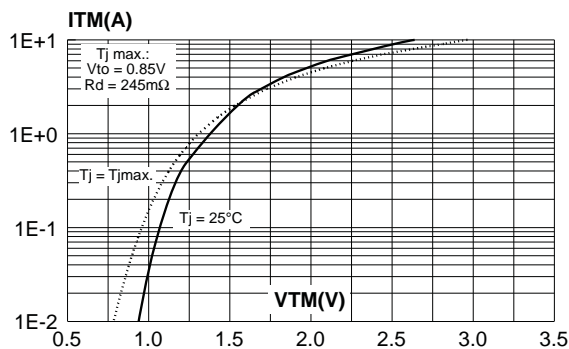


Fig. 7: Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values).

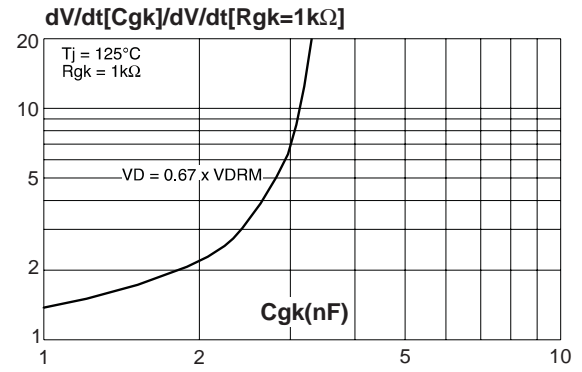
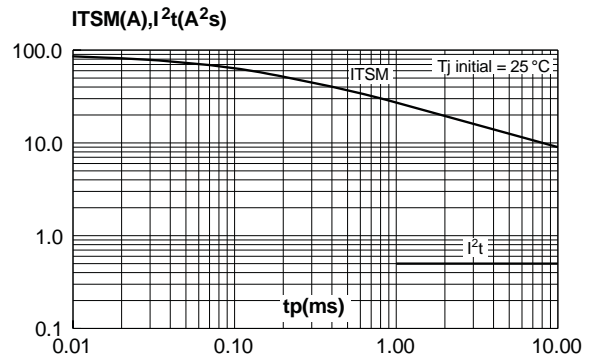
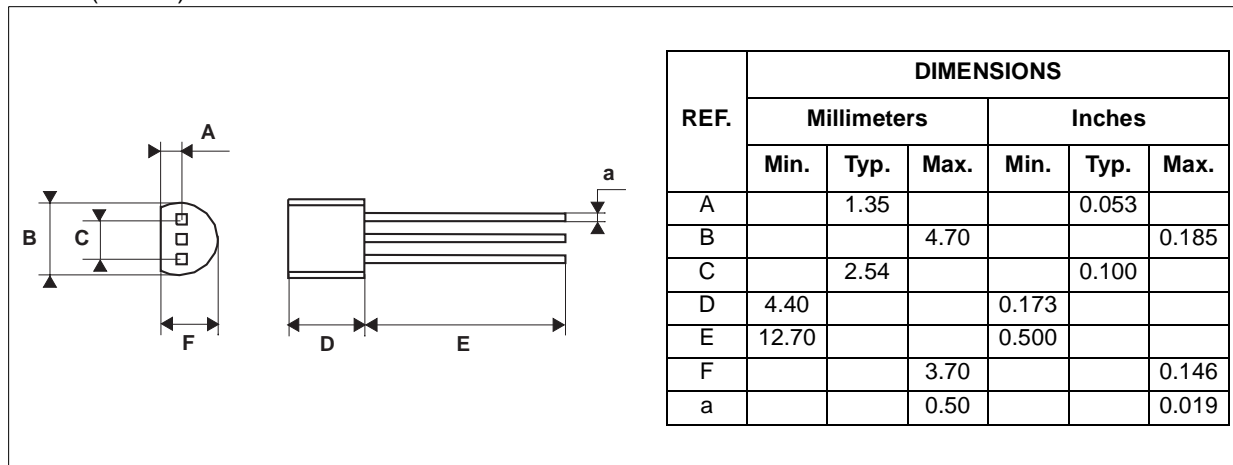


Fig. 9: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms, and corresponding value of I^2t .



PACKAGE MECHANICAL DATA

TO-92 (Plastic)



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