Monolithic Linear IC

LA1805



AM/FM-IF/MPX Tuner System for Radio-Casette Recorders, Music Centers

Overview

The LA1805 is a characteristics-improved version of the LA1810, with the same pin assignment and package as those of the LA1810. Improvements are made on the following point :

- Separation (35dB \rightarrow 48dB) and its dependence on freerunning frequency (Sep – f_F Characteristic on page 4).
- FM main distortion $(0.8\% \rightarrow 0.45\%)$.
- AM detection output (approximately 5dB increased).

The constants on five external parts are changed as shown on page 10.

Functions

- FM-IF : IF amp, quadrature detector, soft muting, tuning indicator.
- MPX : PLL stereo decoder, stereo indicator, forced monaural, VCO stop.
- AM : RF amp, MIX, OSC (with ALC), IF amp, detector, AGC, tuning indicator.

Features

- FM/AM/MPX functions contained on a single chip.
- Minimum number of external parts required.
- On-chip FM muting function.
- High sensitivity.
- Less carrier leak of MPX.

Specifications

Maximum Ratings at Ta=25°C, See specified Test Circuit.

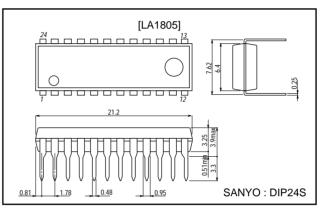
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	Pins 3, 7, 8, 11, 20, 21	9	V
Maximum supply current	I _{CC} max	Pins 3+20+21	50	mA
Flow-in current	ILED	Pins 7, 8	20	mA
(Indicator drive current)				
Flow-out current	I ₂₃	Pin 23	0.1	mA
Allowable power dissipation	Pd max	Ta≤70°C	500	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

SANYO Electric Co., Ltd. Semiconductor Bussiness Headquarters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

Package Dimensions

unit : mm

3067-DIP24S



Operating Conditions at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended operating voltage	Vcc		4.5	V
Operating voltage range	V _{CC} op		3.0 to 8.0	V

Note : The FM output level forms an N curve (LA1805) and an S curve (LA1806).

LA1805 : N curve (for US band)

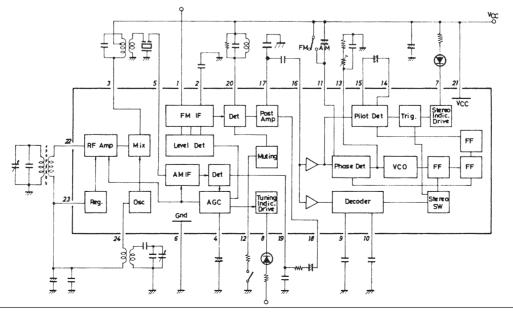
LA1806 : S curve (for Japanese band). Your desired output level can be set by varying the output resistance.

Operating Characteristics at Ta=25°C, V_{CC}=4.5V, See Test Circuit

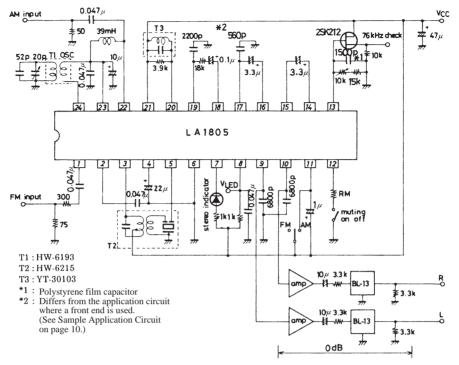
Parameter	Symbol	Conditions		Ratings		
	Gymbol		min	typ	max	Unit
[FM characteristics (Mono) : for	=10.7MHz, f _m =1	kHz]				
Quiescent current	Icco	No input		13	20	mA
-3dB sensitivity	–3dBL.S.	Referenced to VIN=100dBµ,		28	35	dBµ
		100%, down 3dB				
Demodulation output	Vo	V _{IN} =100dBµ, 100% mod.	140	205	280	mV
Channel balance	C.B.	V _{IN} =100dBµ, 100% mod.	0	0	1.5	dB
Total harmonic distortion	THD	V _{IN} =100dBµ, 100% mod.		0.45	1.2	%
Signal to noise ratio	S/N	V _{IN} =100dBµ, 100% mod.	70	80		dB
LED ON sensitivity	V _{LED}	I _L =1mA	23	33	43	dBµ
[FM characteristics (Stereo) : 1	f _c =10.7MHz, f _m =	1kHz, L+R=90%, pilot=10%, V _{IN} =1	00dBµ]			
Separation	Sep		32	48		dB
Stereo distortion	THD (Main)			0.45	1.2	%
LED ON level	V _{LED} -on		2.4	3.9	5.4	%
LED OFF level	V _{LED} -off			2.7		%
[AM characteristics : f _c =1000k	Hz, f _m =1kHz]		!		!	
Quiescent current	Icco	No input		9.5	14.5	mA
Demodulation output	V _O 1	V _{IN} =23dBµ, 30% mod.	27	50	90	mV
	V _O 2	V _{IN} =80dBµ, 30% mod.	70	113	173	mV
Signal to noise ratio	S/N1	V _{IN} =23dBµ, 30% mod.	17	21		dB
	S/N2	V _{IN} =80dBµ, 30% mod.	50	55		dB
Total harmonic distortion	THD1	V _{IN} =80dBµ, 30% mod.		0.45	1.2	%
	THD2	V _{IN} =100dBµ, 30% mod.		0.6	1.5	%
LED on sensitivity	V _{LED}	I _L =1mA	16	24	32	dBµ

Note : Be fully careful of dielectric breakdown.

Equivalent Circuit Block Diagram



Test Circuit

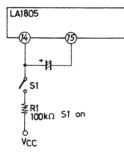


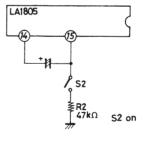
How to use the LA1805

1. Forced monaural mode.

Figures 1 and 2 show how to cause the forced monaural mode to be entered.

- (1) Connect pin 14 to V_{CC} through a resistor of $100k\Omega$. (Turn ON the SW1 in Figure 1.)
- (2) Connect pin 15 to GND through a resistor of $47k\Omega$. (Turn ON the SW2 in Figure 2.)



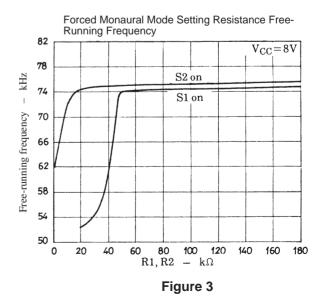




Either above-mentioned (1) or (2) causes the forced monaural mode to be entered. In this case, the VCO does not stop operating. If the resistance of R1 and R2 are decreased, internal bias will vary when the S1 or S2 is turned ON. This data is shown in Figure 3.

Figure 2 Forced Monaural Mode Setting Method

Unit (resistance : Ω, capacitance : F)

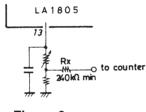


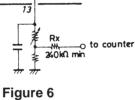
2. VCO Stop

The VCO is so designed as to stop automatically at the AM mode.

(when pin 11 and pin 21 are at the same potential) There is no pin available for stopping the VCO at the FM mode. However, the method shown right can be used to stop the VCO at the FM mode, causing the forced monaural mode to be entered.

- 3. Free-running frequency measurement and adjustment Either of the following two methods is used to measure the free-running frequency.
 - (1) Connect pin 13 to a frequency counter through the high input impedance amplifier.
 - (2) Connect the connection point of the semifixed resistor connected to pin 13 and the fixed resistor to a frequency counter through the resistor of $240k\Omega$ or greater.





Make the wiring as short as possible 474 SW Figure 4 LA1805 12 -o Vcc o to counter Figure 5 $\Delta f -$ Rx f 0 % -1 Measured error, Δf -2 -3 -4 $V_{CC} = 4.5V$ IF input 100dBµ Nonmodulation -5 50 100 150 200 250 300 0 Resistor value, RX kΩ _

LA1805

13

14

15

How the error changes with the resistor value is shown in Figure 7.

Figure 7 • When setting the free-running frequency, the following must be noted. Apply a 10.7MHz 100dBµ nonmodulation carrier as IF input signal and set to 76kHz±50Hz with the tuning indicator lighted. _

4. Separation setting capacitor Cs

The separation characteristic for the LA1805 alone (IF input) differs from that for the antenna input with a front end. This difference is caused by the characteristics of the front end and ceramic filter. Shown right is how the separation characteristic changes with the separation setting capacitor value when the LA1186N is used as front end. Referring to this separation characteristic, choose the optimum separation for your set model.

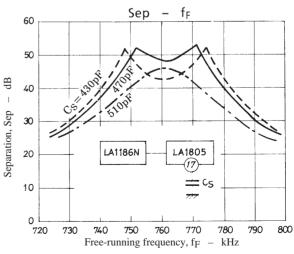
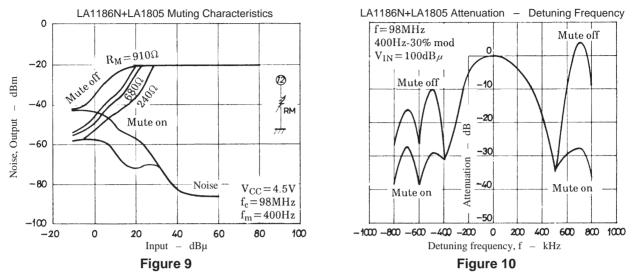


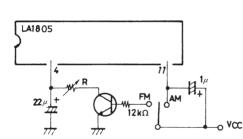
Figure 8

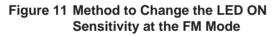
5. FM muting pin

The external resistor connected to pin 12 can be used to vary the muting level (Figure 9). The abnormal sound at the time of side peak reception at the FM mode can be reduced by weak signal muting.



6. The following method can be used to change the LED ON sensitivity at the FM mode (Figure 11). The data on the LED ON sensitivity setting resistance and LED ON sensitivity is shown in Figure 12.

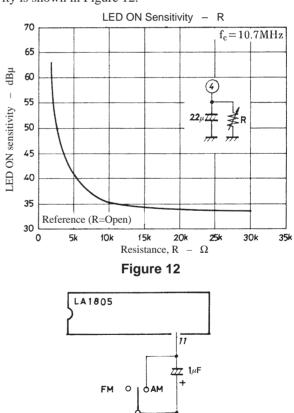






7. AM-FM selection

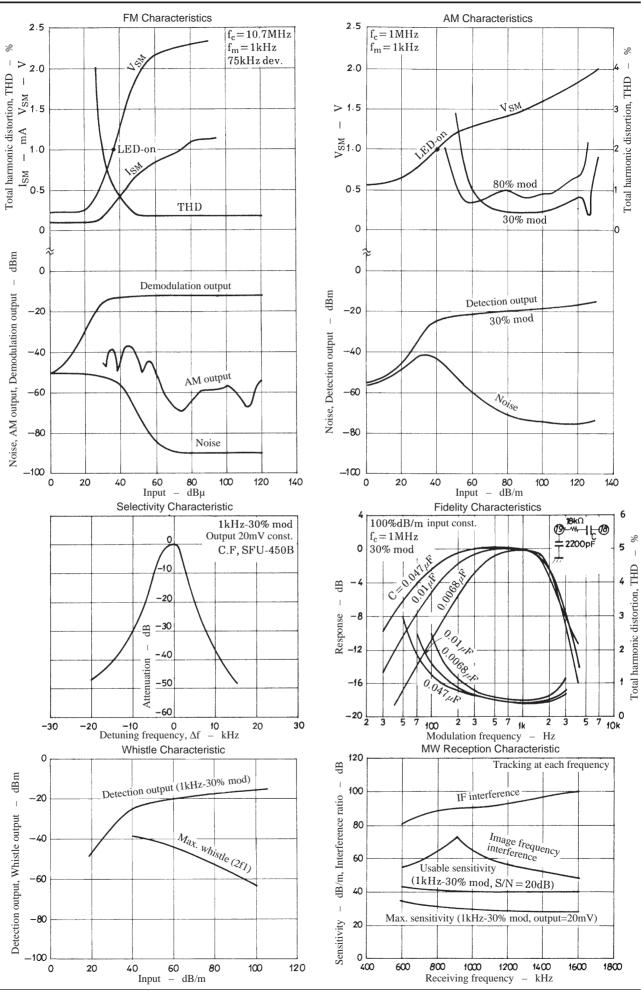
The FM mode is entered with pin 11 open as shown in Figure 13. When pin 11 and pin 21 are made to be at the same potential in terms of DC, the AM mode is entered. It should be noted that the dynamic range is narrowed whether the potential at pin 11 is lower or higher than that at pin 21.

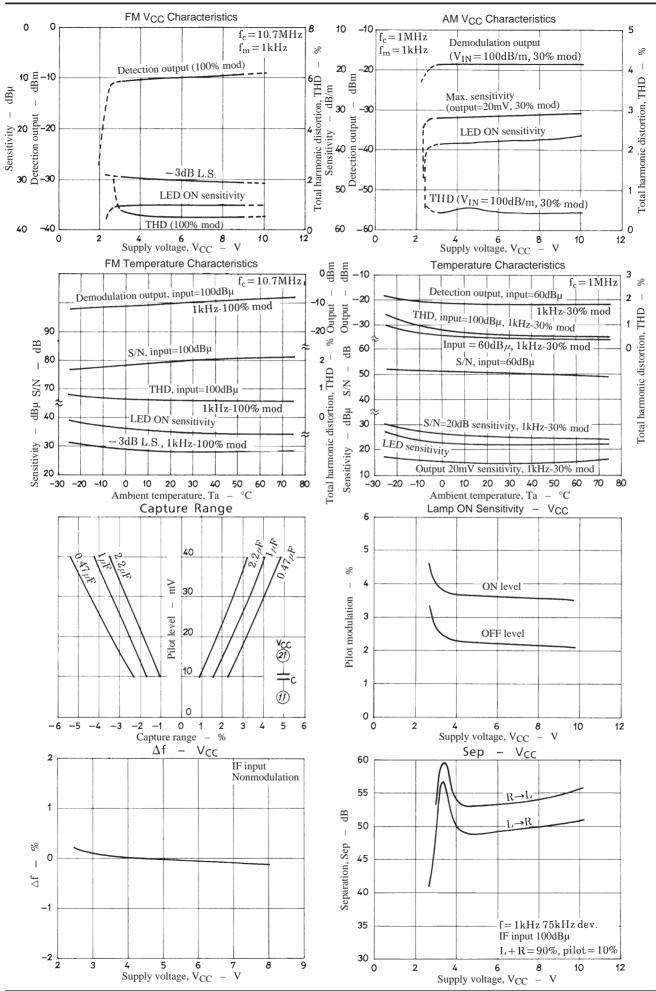


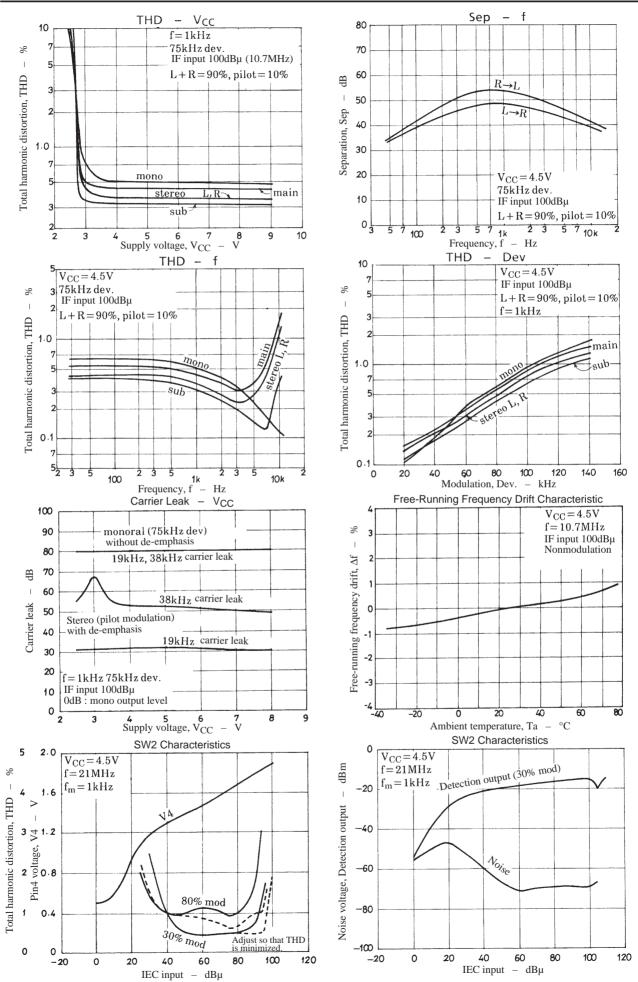


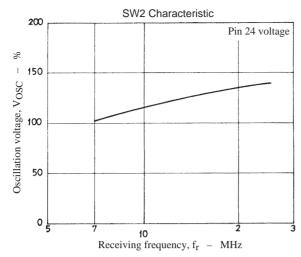
CC (21pin)

8. If a noise appears in the detection output when the tuning LED goes ON at the AM mode, connect a capacitor across pin 8 and GND to eliminate the noise.

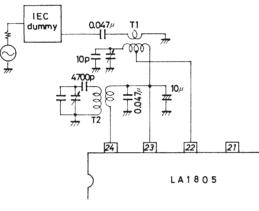


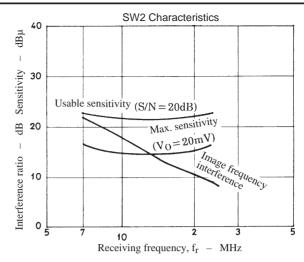




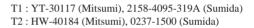


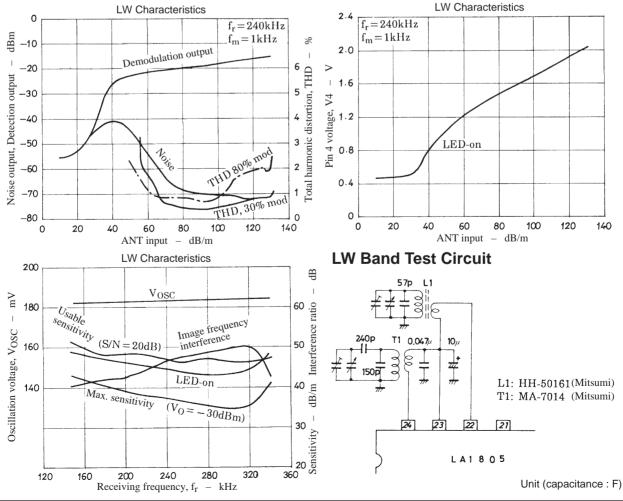


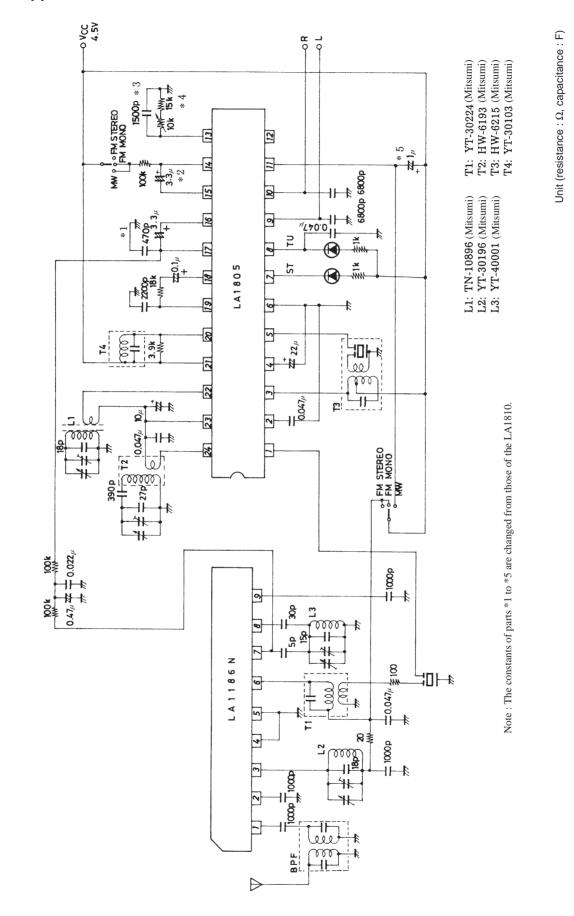




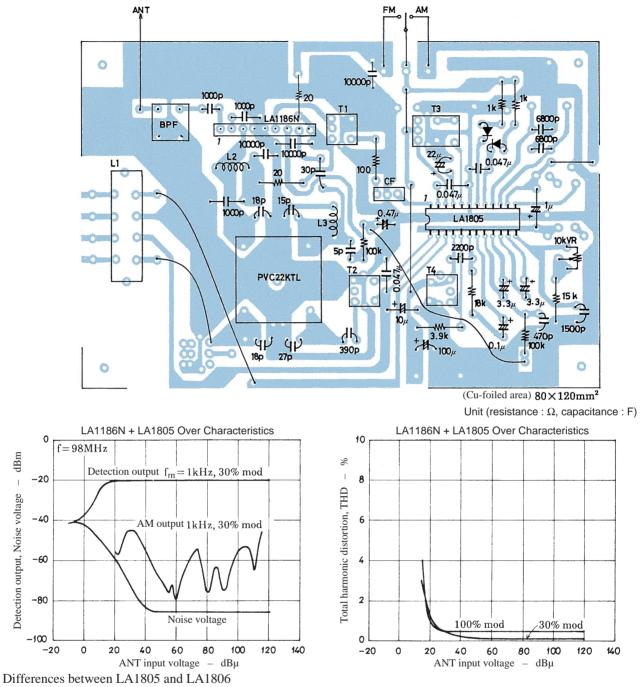
Unit (capacitance : F)







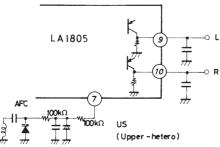
Sample Application Circuit : LA1186N+LA1805 FM/MW

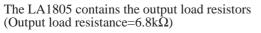


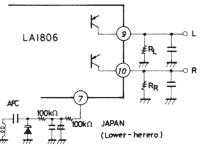
Sample Printed Circuit Pattern (See Sample Application Circuit.)



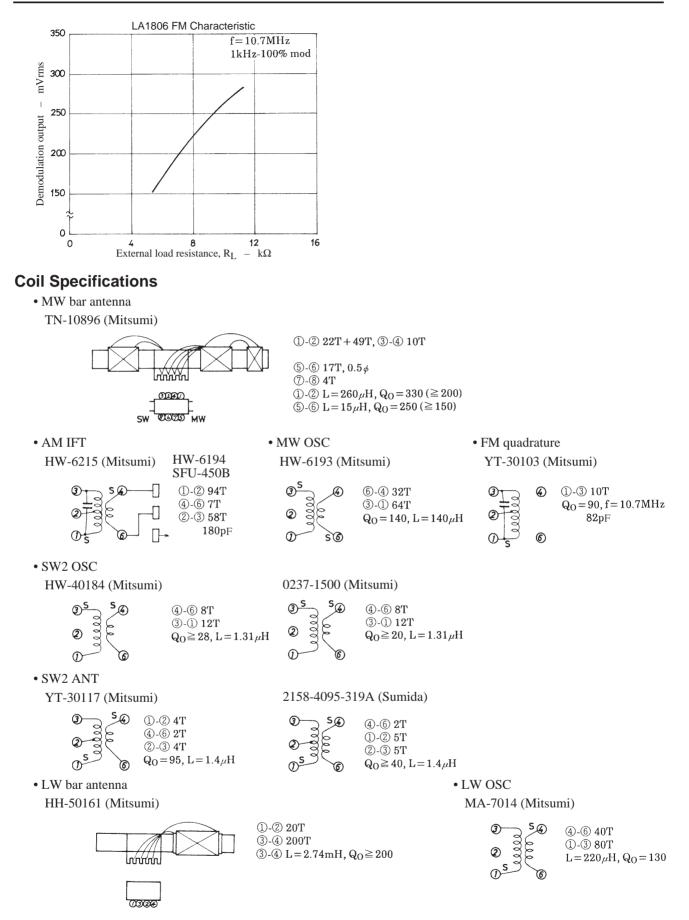
- (1) Same pin assignment
- (2) The internal circuit of the MPX OUT (pin 9, pin 10) is different as shown below.
 - The FM S curve at pin 17 is in the opposite direction and the circuit in which AFC is provided is the same for the US band and Japanese band.







For the LA1806, output load resistors RL, RR are connectedxternally. The graph of demodulation output vs. R_L (R_R) is shown below.



■ No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.

Anyone purchasing any products described or contained herein for an above-mentioned use shall:

Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:

- ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of January, 1998. Specifications and information herein are subject to change without notice.