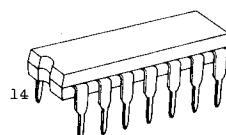
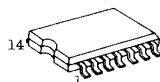


TC4071BP/TC4071BF QUAD 2 INPUT OR GATE  
TC4072BP/TC4072BF DUAL 4 INPUT OR GATE  
TC4075BP/TC4075BF TRIPLE 3 INPUT OR GATE

TC4071BP/BF, TC4075BP/BF and TC4072BP/BF are positive logic OR gates with two inputs, three inputs and four inputs respectively. As all the outputs of gates are equipped with the buffer circuits of inverters, the input/output propagation characteristic has been improved and the variation of propagation time caused by increase of load capacity is kept minimum.



DIP14(3D14A-P)



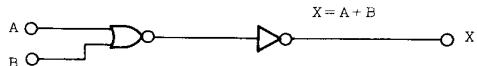
MFP14(F14GB-P)

#### ABSOLUTE MAXIMUM RATINGS

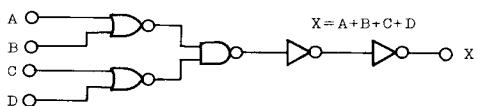
CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V <sub>DD</sub>	V <sub>SS</sub> -0.5 ~ V <sub>SS</sub> +20	V
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.5 ~ V <sub>DD</sub> +0.5	V
DC Input Current	I <sub>IN</sub>	±10	mA
Power Dissipation	P <sub>D</sub>	300(DIP)/180(MFP)	mW
Operating Temperature Range	T <sub>A</sub>	-40 ~ 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 ~ 150	°C
Lead Temp./Time	T <sub>sol</sub>	260°C • 10 sec	

#### LOGIC DIAGRAM

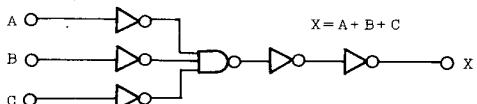
1/4 TC4071BP/BF



1/2 TC4072BP/BF

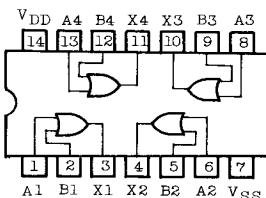


1/3 TC4075BP/BF

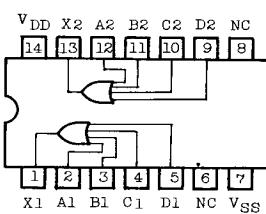


#### PIN ASSIGNMENT (TOP VIEW)

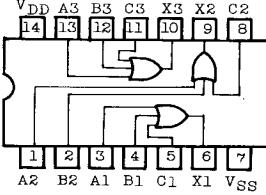
TC4071BP/BF



TC4072BP/BF



TC4075BP/BF



RECOMMENDED OPERATING CONDITIONS ( $V_{SS}=0V$ )

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNITS
DC Supply Voltage	$V_{DD}$		3	-	18	V
Input Voltage	$V_{IN}$		0	-	$V_{DD}$	V

STATIC ELECTRICAL CHARACTERISTICS ( $V_{SS}=0V$ )

CHARACTERISTIC	SYMBOL	TEST CONDITION	$V_{DD}$ (V)	-40°C		25°C		85°C		UNITS		
				MIN.	MAX.	MIN.	TYP.	MAX.	MIN.			
High-Level Output Voltage	$V_{OH}$	$ I_{OUT}  < 1\mu A$ $V_{IN}=V_{SS}, V_{DD}$	5	4.95	-	4.95	5.00	-	4.95	-	V	
			10	9.95	-	9.95	10.00	-	9.95	-		
			15	14.95	-	14.95	15.00	-	14.95	-		
Low-Level Output Voltage	$V_{OL}$	$ I_{OUT}  < 1\mu A$ $V_{IN}=V_{SS}$	5	-	0.05	-	0.00	0.05	-	0.05	V	
			10	-	0.05	-	0.00	0.05	-	0.05		
			15	-	0.05	-	0.00	0.05	-	0.05		
Output High Current	$I_{OH}$	$V_{OH}=4.6V$ $V_{OH}=2.5V$ $V_{OH}=9.5V$ $V_{OH}=13.5V$ $V_{IN}=V_{SS}, V_{DD}$	5	-0.61	-	-0.51	-1.0	-	-0.42	-	mA	
			5	-2.5	-	-2.1	-4.0	-	-1.7	-		
			10	-1.5	-	-1.3	-2.2	-	-1.1	-		
			15	-4.0	-	-3.4	-9.0	-	-2.8	-		
			5	0.61	-	0.51	1.5	-	0.42	-		
Output Low Current	$I_{OL}$	$V_{OL}=0.4V$ $V_{OL}=0.5V$ $V_{OL}=1.5V$	10	1.5	-	1.3	3.8	-	1.1	-	mA	
			15	4.0	-	3.4	15.0	-	2.8	-		
			5	3.5	-	3.5	2.75	-	3.5	-		
		$V_{IN}=V_{SS}$	10	7.0	-	7.0	5.5	-	7.0	-		
Input High Voltage	$V_{IH}$	$V_{OUT}=4.5V$ $V_{OUT}=9.0V$ $V_{OUT}=13.5V$	5	3.5	-	3.5	2.75	-	3.5	-	V	
			10	7.0	-	7.0	5.5	-	7.0	-		
			15	11.0	-	11.0	8.25	-	11.0	-		
		$ I_{OUT}  < 1\mu A$	5	-	1.5	-	2.25	1.5	-	1.5	-	
Input Low Voltage	$V_{IL}$	$V_{OUT}=0.5V, 4.5V$ $V_{OUT}=1.0V, 9.0V$ $V_{OUT}=1.5V, 13.5V$	5	-	1.5	-	2.25	1.5	-	1.5	V	
			10	-	3.0	-	4.5	3.0	-	3.0		
			15	-	4.0	-	6.75	4.0	-	4.0		
		$ I_{OUT}  < 1\mu A$	5	-	1.5	-	2.25	1.5	-	1.5		
Input Current	"H" Level	$I_{IH}$	$V_{IH}=18V$	18	-	0.1	-	$10^{-5}$	0.1	-	1.0	$\mu A$
	"L" Level	$I_{IL}$	$V_{IL}=0V$	18	-	-0.1	-	$-10^{-5}$	-0.1	-	-1.0	
Quiescent Device Current	$I_{DD}$	$V_{IN}=V_{SS}, V_{DD}$	5	-	0.25	-	0.001	0.25	-	7.5	$\mu A$	
			*	-	0.5	-	0.001	0.5	-	15		
			10	-	1.0	-	0.002	1.0	-	30		

\* All valid input combinations.

# TC4071BP/BF, TC4072BP/BF, TC4075BP/BF

DYNAMIC ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ C$ ,  $V_{SS}=0V$ ,  $C_L=50pF$ )

CHARACTERISTIC	SYMBOL	TEST CONDITION	$V_{DD}(V)$	MIN.	TYP.	MAX.	UNITS
Output Transition Time (TC4072BP/BF) (TC4075BP/BF)	$t_{TLH}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Output Transition Time (TC4072BP/BF) (TC4075BP/BF)	$t_{THL}$		5	-	80	200	ns
			10	-	50	100	
			15	-	40	80	
Output Transition Time (TC4071BP/BF)	$t_{TLH}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Output Transition Time (TC4071BP/BF)	$t_{THL}$		5	-	70	200	ns
			10	-	35	100	
			15	-	30	80	
Propagation Delay Time (TC4071BP/BF)	$t_{pLH}$		5	-	65	200	ns
			10	-	30	100	
			15	-	25	80	
Propagation Delay Time (TC4071BP/BF)	$t_{pHL}$		5	-	65	200	ns
			10	-	30	100	
			15	-	25	80	
Propagation Delay Time (TC4072BP/BF)	$t_{pLH}$		5	-	115	250	ns
			10	-	45	120	
			15	-	30	90	
Propagation Delay Time (TC4072BP/BF)	$t_{pHL}$		5	-	115	250	ns
			10	-	45	120	
			15	-	30	90	
Propagation Delay Time (TC4075BP/BF)	$t_{pLH}$		5	-	95	250	ns
			10	-	40	120	
			15	-	30	90	
Propagation Delay Time (TC4075BP/BF)	$t_{pHL}$		5	-	95	250	ns
			10	-	40	120	
			15	-	30	90	
Input Capacitance	$C_{IN}$			-	5	7.5	pF

## CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

