PHYSICAL DIMENSIONS:

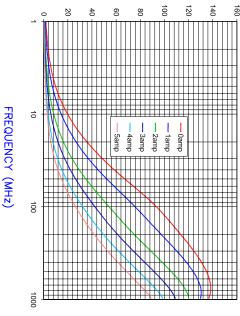
HI1812V101R-10

UNCONTROLLED DOCUMENT

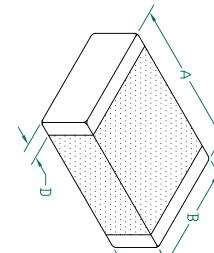
- ϖ 3.20 [.126] ± 0.25 [.010]
- 2.30 [.091]
- 0.25 [.010] 0.25 [.010]
- 0.65 [.026]

± 0.45 [.018]

Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



IMPEDANCE (Ω)



Maximum

125 75

0.007

8000

M A

Minimum Nominal

@ 100MHz

DCR (S)

Current Rated

(S)

100

ELECTRICAL CHARACTERISTICS:



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2500 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN
- COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.



4.22 [.166] 5.90 [.232] 2.57 [.101]

|Z| , R, AND X vs. FREQUENCY

(For wave soldering, add 0.762

	TEMPERATU [266]	RE (℃) [℉]	_250_	
100 SECONDS 5 - 10 SECONDS	80 SECONDS		REFLOW SOLDERING 50 PRE-HEATING SOLDERING	RECOMMENDED SOLDERING CONDITIONS
60 SECONDS			NATURAL	DITIONS

	ı		DATE INT HI1812V101R-10-C	N	DATE	DESCRIPTION	REV				REF. 3298	HP 16194A Test Fixture. TEST REF. 3298	6194A 1	Ħ 1
2 of 2	2	100L #		TMB	14/02/04	ORIGINAL DRAFT 04	>				/Material Analyzer	AGILENT E4991A Rt Impedance/Material Analyzer	N E49	AGIL
	- SHEET:	SCALE		支	1/23/08	HS SYMBOL	В			<u> </u>			1	
				JUN	11/06/09	UPDATE THE DCR AND D 11	ဂ	,		<	,	1	1	
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IMPEDANCE (Ω)

120 140 160 T

Mouser Electronics

Authorized Distributor

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<u>Laird Technologies</u>:

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