

TEXAS INSTRUMENTS

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LM150/LM350A/LM350 3-Amp Adjustable Regulators

Check for Samples: LM150, LM350-N, LM350A

FEATURES

- Adjustable Output Down to 1.2V
- Guaranteed 3A output Current
- Guaranteed Thermal Regulation
- Output is Short Circuit Protected
- Current Limit Constant with Temperature
- P⁺ Product Enhancement Tested
- 86 dB Ripple Rejection
- Ensured 1% Output Voltage Tolerance (LM350A)
- Ensured Max. 0.01%/V Line Regulation (LM350A)
- Ensured Max. 0.3% Load Regulation (LM350A)

APPLICATIONS

- Adjustable Power supplies
- Constant Current Regulators
- Battery Chargers

DESCRIPTION

The LM150 series of adjustable 3-terminal positive voltage regulators is capable of supplying in excess of 3A over a 1.2V to 33V output range. They are exceptionally easy to use and require only 2 external resistors to set the output voltage. Further, both line and load regulation are comparable to discrete designs. Also, the LM150 is packaged in standard transistor packages which are easily mounted and handled.

In addition to higher performance than fixed regulators, the LM150 series offers full overload protection available only in IC's. Included on the chip are current limit, thermal overload protection and safe area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is accidentally disconnected.

Normally, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors in which case an input bypass is needed. An output capacitor can be added to improve transient response, while bypassing the adjustment pin will increase the regulator's ripple rejection.

Besides replacing fixed regulators or discrete designs, the LM150 is useful in a wide variety of other applications. Since the regulator is "floating" and sees only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input to output differential is not exceeded, i.e., avoid short-circuiting the output.

By connecting a fixed resistor between the adjustment pin and output, the LM150 can be used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to ground which programs the output to 1.2V where most loads draw little current.

The part numbers in the LM150 series which have a NDS suffix are packaged in a standard Steel TO-3 package, while those with a NDE suffix are packaged in a TO-220 plastic package. The LM150 is rated for $-55^{\circ}C \leq T_{J} \leq +150^{\circ}C$, while the LM350A is rated for $-40^{\circ}C \leq T_{J} \leq +125^{\circ}C$, and the LM350 is rated for $0^{\circ}C \leq T_{J} \leq +125^{\circ}C$.

Connection Diagram



Case is Output

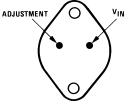


Figure 1. (TO-3 STEEL) Metal Can Package Bottom View See Package Number NDS0002A

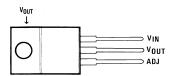


Figure 2. (TO-220) Plastic Package Front View See Package Number NDE0003B

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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings⁽¹⁾⁽²⁾⁽³⁾

| 5 | | | | |
|-----------------------------------|-------------------------------------|---------------------------------|--|--|
| Power Dissipation | | Internally Limited | | |
| Input-Output Voltage Differential | | +35V | | |
| Storage Temperature | | −65°C to +150°C | | |
| | Metal Package (Soldering, 10 sec.) | 300°C | | |
| Lead Temperature | Plastic Package (Soldering, 4 sec.) | 260°C | | |
| ESD Tolerance | TBD | | | |
| | LM150 | −55°C ≤ T _J ≤ +150°C | | |
| Operating Temperature Range | LM350A | −40°C ≤ T _J ≤ +125°C | | |
| | LM350 | 0°C ≤ T _J ≤ +125°C | | |

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics.

(2) Refer to RETS150K drawing for military specifications of the LM150K.

(3) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

Electrical Characteristics

Specifications with standard type face are for $T_J= 25^{\circ}$ C, and those with **boldface type** apply over **full Operating Temperature Range.** Unless otherwise specified, $V_{IN}-V_{OUT}= 5V$, and $I_{OUT}= 10 \text{ mA}^{(1)}$

| Parameter | Conditions | | 11-11- | | | |
|---|---|------|--------|------|-------|--|
| | | Min | Тур | Max | Units | |
| Reference Voltage | $3V \le (V_{IN} - V_{OUT}) \le 35V$, 10 mA $\le I_{OUT} \le 3A$, P $\le 30W$ | 1.20 | 1.25 | 1.30 | V | |
| Line Degulation | $3V \leq (V_{IN} - V_{OUT}) \leq 35V^{(2)}$ | | 0.005 | 0.01 | %/V | |
| Line Regulation | $3V \leq (V_{IN} - V_{OUT}) \leq 35V$ | | 0.02 | 0.05 | %/V | |
| Lood Degulation | $10 \text{ mA} \le I_{\text{OUT}} \le 3A^{(2)}$ | | 0.1 | 0.3 | % | |
| Load Regulation | $10 \text{ IIA} \ge 1_{\text{OUT}} \ge 3A^{-7}$ | | 0.3 | 1 | % | |
| Thermal Regulation | 20 ms Pulse | | 0.002 | 0.01 | %/W | |
| Adjustment Pin Current | | | 50 | 100 | μA | |
| Adjustment Pin Current Change | 10 mA \leq I _{OUT} \leq 3A, 3V \leq (V _{IN} - V _{OUT}) \leq 35V | | 0.2 | 5 | μA | |
| Temperature Stability | $T_{MIN} \le T_J \le T_{MAX}$ | | 1 | | % | |
| Minimum Load Current | $V_{IN} - V_{OUT} = 35V$ | | 3.5 | 5 | mA | |
| | $V_{IN} - V_{OUT} \le 10V$ | 3.0 | 4.5 | | А | |
| Current Limit | $V_{IN} - V_{OUT} = 30V$ | 0.3 | 1 | | А | |
| RMS Output Noise, % of V _{OUT} | 10 Hz ≤ f ≤ 10 kHz | | 0.001 | | % | |
| Dinala Deiestian Detie | V _{OUT} = 10V, f = 120 Hz, C _{ADJ} = 0 µF | | 65 | | dB | |
| Ripple Rejection Ratio | V _{OUT} = 10V, f = 120 Hz, C _{ADJ} = 10 µF | 66 | 86 | | dB | |
| Long-Term Stability | T _J = 125°C, 1000 hrs | | 0.3 | 1 | % | |
| Thermal Resistance, Junction to Case | NDS Package | | 1.2 | 1.5 | °C/W | |
| Thermal Resistance, Junction to Ambient (No Heat Sink) NDS Package | | | 35 | | °C/W | |

(1) These specifications are applicable for power dissipations up to 30W for the TO-3 (NDS) package and 25W for the TO-220 (NDE) package. Power dissipation is ensured at these values up to 15V input-output differential. Above 15V differential, power dissipation will be limited by internal protection circuitry. All limits (i.e., the numbers in the Min. and Max. columns) are ensured to AOQL (Average Outgoing Quality Level).

(2) Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specifications for thermal regulation.



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Electrical Characteristics

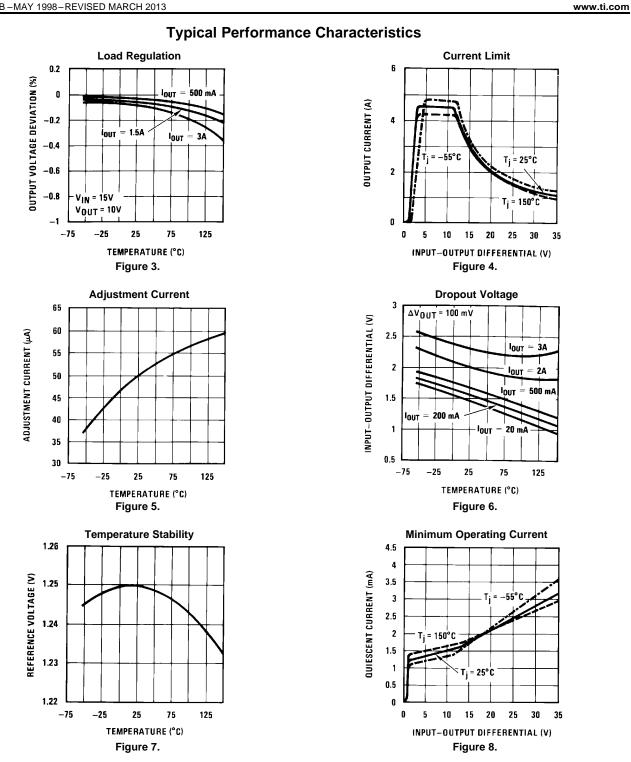
Specifications with standard type face are for $T_J = 25^{\circ}$ C, and those with **boldface type** apply over **full Operating Temperature Range.** Unless otherwise specified, $V_{IN} - V_{OUT} = 5V$, and $I_{OUT} = 10$ mA.⁽¹⁾

| Parameter | Conditions | | LM350A | | | LM350 | | |
|---|---|-------|--------|-------|------|-------|------|------|
| Parameter | Conditions | Min | Тур | Max | Min | Тур | Max | |
| | I _{OUT} = 10 mA, T _J = 25°C | 1.238 | 1.250 | 1.262 | | | | V |
| Reference Voltage | $3V \le (V_{IN} - V_{OUT}) \le 35V$, 10 mA $\le I_{OUT} \le 3A$, P $\le 30W$ | 1.225 | 1.250 | 1.270 | 1.20 | 1.25 | 1.30 | V |
| Line Degulation | $3V \leq (V_{IN} - V_{OUT}) \leq 35V^{(2)}$ | | 0.005 | 0.01 | | 0.005 | 0.03 | %/V |
| Line Regulation | | | 0.02 | 0.05 | | 0.02 | 0.07 | %/V |
| Lood Degulation | 10 - 0.51 - 5.20(2) | | 0.1 | 0.3 | | 0.1 | 0.5 | % |
| Load Regulation | $10 \text{ mA} \le I_{\text{OUT}} \le 3A^{(2)}$ | | 0.3 | 1 | | 0.3 | 1.5 | % |
| Thermal Regulation | 20 ms Pulse | | 0.002 | 0.01 | | 0.002 | 0.03 | %/W |
| Adjustment Pin Current | | | 50 | 100 | | 50 | 100 | μA |
| Adjustment Pin Current Change | 10 mA \leq I _{OUT} \leq 3A, 3V \leq (V _{IN} – V _{OUT}) \leq 35V | | 0.2 | 5 | | 0.2 | 5 | μA |
| Temperature Stability | $T_{MIN} \le T_J \le T_{MAX}$ | | 1 | | | 1 | | % |
| Minimum Load Current | $V_{IN} - V_{OUT} = 35V$ | | 3.5 | 10 | | 3.5 | 10 | mA |
| | $V_{IN} - V_{OUT} \le 10V$ | 3.0 | 4.5 | | 3.0 | 4.5 | | А |
| Current Limit | $V_{IN} - V_{OUT} = 30V$ | 0.3 | 1 | | 0.25 | 1 | | А |
| RMS Output Noise, % of V _{OUT} | 10 Hz ≤ f ≤ 10 kHz | | 0.001 | | | 0.001 | | % |
| Dianta Datiantian Datia | V _{OUT} = 10V, f = 120 Hz, C _{ADJ} = 0 μF | | 65 | | | 65 | | dB |
| Ripple Rejection Ratio | V _{OUT} = 10V, f = 120 Hz, C _{ADJ} = 10 μF | 66 | 86 | | 66 | 86 | | dB |
| Long-Term Stability | T _J = 125°C, 1000 hrs | | 0.25 | 1 | | 0.25 | 1 | % |
| Thermal Resistance, Junction to | NDS Package | | | | | 1.2 | 1.5 | °C/W |
| Case | NDE Package | | 3 | 4 | | 3 | 4 | °C/W |
| Thermal Resistance, Junction to | NDS Package | | | | | 35 | | °C/W |
| Ambient (No Heat Sink) | NDE Package | | 50 | | | 50 | | °C/W |

(1) These specifications are applicable for power dissipations up to 30W for the TO-3 (NDS) package and 25W for the TO-220 (NDE) package. Power dissipation is ensured at these values up to 15V input-output differential. Above 15V differential, power dissipation will be limited by internal protection circuitry. All limits (i.e., the numbers in the Min. and Max. columns) are ensured to AOQL (Average Outgoing Quality Level).

(2) Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specifications for thermal regulation.





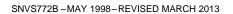
4



1M

100k

1M



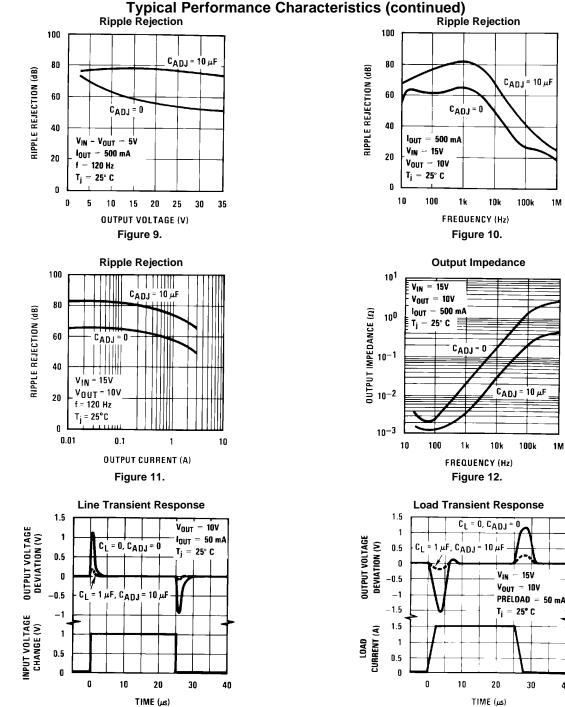


Figure 14.

Figure 13.

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(1)

APPLICATION HINTS

In operation, the LM150 develops a nominal 1.25V reference voltage, V_{REF} , between the output and adjustment terminal. The reference voltage is impressed across program resistor R1 and, since the voltage is constant, a constant current I_1 then flows through the output set resistor R2, giving an output voltage of

$$V_{OUT} = V_{REF} \left(1 + \frac{R^2}{R^1} \right) + I_{ADJ} R^2.$$

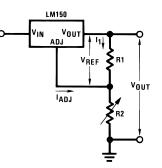


Figure 15.

Since the 50 μ A current from the adjustment terminal represents an error term, the LM150 was designed to minimize I_{ADJ} and make it very constant with line and load changes. To do this, all quiescent operating current is returned to the output establishing a minimum load current requirement. If there is insufficient load on the output, the output will rise.

EXTERNAL CAPACITORS

An input bypass capacitor is recommended. A 0.1 μ F disc or 1 μ F solid tantalum on the input is suitable input bypassing for almost all applications. The device is more sensitive to the absence of input bypassing when adjustment or output capacitors are used but the above values will eliminate the possibility of problems.

The adjustment terminal can be bypassed to ground on the LM150 to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. With a 10 μ F bypass capacitor 86 dB ripple rejection is obtainable at any output level. Increases over 10 μ F do not appreciably improve the ripple rejection at frequencies above 120 Hz. If the bypass capacitor is used, it is sometimes necessary to include protection diodes to prevent the capacitor from discharging through internal low current paths and damaging the device.

In general, the best type of capacitors to use is solid tantalum. Solid tantalum capacitors have low impedance even at high frequencies. Depending upon capacitor construction, it takes about 25 μ F in aluminum electrolytic to equal 1 μ F solid tantalum at high frequencies. Ceramic capacitors are also good at high frequencies, but some types have a large decrease in capacitance at frequencies around 0.5 MHz. For this reason, 0.01 μ F disc may seem to work better than a 0.1 μ F disc as a bypass.

Although the LM150 is stable with no output capacitors, like any feedback circuit, certain values of external capacitance can cause excessive ringing. This occurs with values between 500 pF and 5000 pF. A 1 μ F solid tantalum (or 25 μ F aluminum electrolytic) on the output swamps this effect and insures stability.

LOAD REGULATION

The LM150 is capable of providing extremely good load regulation but a few precautions are needed to obtain maximum performance. The current set resistor connected between the adjustment terminal and the output terminal (usually 240 Ω) should be tied directly to the output (case) of the regulator rather than near the load. This eliminates line drops from appearing effectively in series with the reference and degrading regulation. For example, a 15V regulator with 0.05 Ω resistance between the regulator and load will have a load regulation due to line resistance of $0.05\Omega \times I_{OUT}$. If the set resistor is connected near the load the effective line resistance will be 0.05Ω (1 + R2/R1) or in this case, 11.5 times worse.

Figure 16 shows the effect of resistance between the regulator and 240Ω set resistor.



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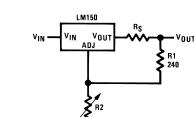


Figure 16. Regulator with Line Resistance in Output Lead

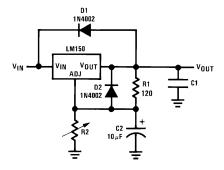
With the TO-3 package, it is easy to minimize the resistance from the case to the set resistor, by using two separate leads to the case. The ground of R2 can be returned near the ground of the load to provide remote ground sensing and improve load regulation.

PROTECTION DIODES

When external capacitors are used with *any* IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator. Most 10 μ F capacitors have low enough internal series resistance to deliver 20A spikes when shorted. Although the surge is short, there is enough energy to damage parts of the IC.

When an output capacitor is connected to a regulator and the input is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of the capacitor, the output voltage of the regulator, and the rate of decrease of $V_{\rm IN}$. In the LM150, this discharge path is through a large junction that is able to sustain 25A surge with no problem. This is not true of other types of positive regulators. For output capacitors of 25 μ F or less, there is no need to use diodes.

The bypass capacitor on the adjustment terminal can discharge through a low current junction. Discharge occurs when *either* the input or output is shorted. Internal to the LM150 is a 50Ω resistor which limits the peak discharge current. No protection is needed for output voltages of 25V or less and 10 µF capacitance. Figure 17 shows an LM150 with protection diodes included for use with outputs greater than 25V and high values of output capacitance.



D1 protects against C1 D2 protects against C2

Figure 17. Regulator with Protection Diodes

$$V_{OUT} = 1.25V \left(1 + \frac{R2}{R1}\right) + I_{ADJ}R2$$

(2)

Schematic Diagram

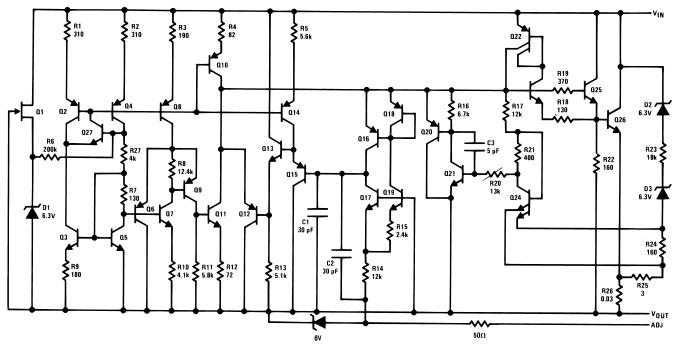
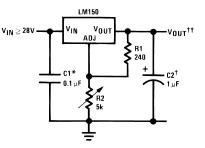


Figure 18. Schematic Diagram

Typical Applications



Full output current not available

at high input-output voltages.

†Optional—improves transient response. Output capacitors in the range of 1 μF to 1000 μF of aluminum or tantalum electrolytic are commonly used to provide improved output impedance and rejection of transients. *Needed if device is more than 6 inches from filter capacitors.

Figure 19. 1.2V–25V Adjustable Regulator

$$\dagger \dagger V_{OUT} = 1.25V \left(1 + \frac{R^2}{R^2}\right) + I_{ADJ} (R^2)$$

Note: Usually R1 = 240Ω for LM150 and R1 = 120Ω for LM350.

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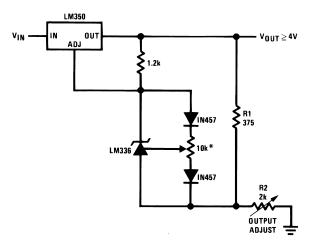
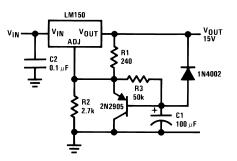
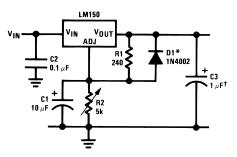




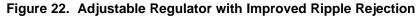
Figure 20. Precision Power Regulator with Low Temperature Coefficient







†Solid tantalum
*Discharges C1 if output is shorted to ground

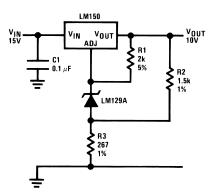


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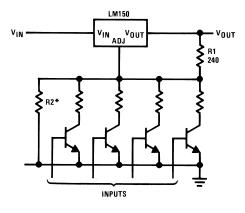


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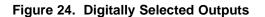
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*Sets maximum V_{OUT}



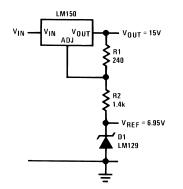
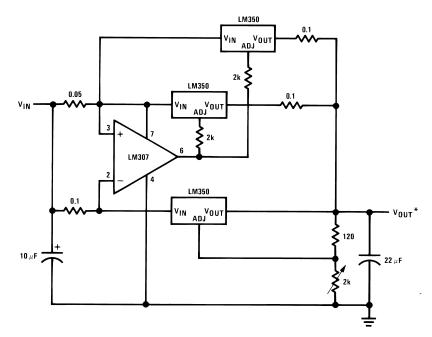


Figure 25. Regulator and Voltage Reference

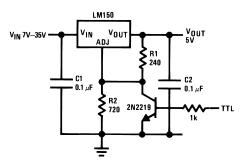


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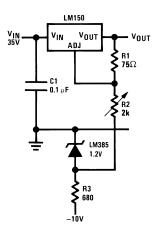
*Minimum load current 50 mA





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*Min output ≈ 1.2V
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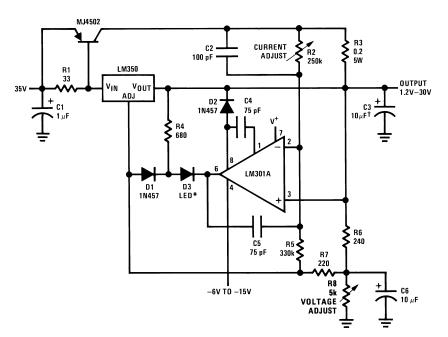


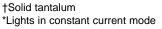
Full output current not available at high input-output voltages

Figure 28. 0 to 30V Regulator

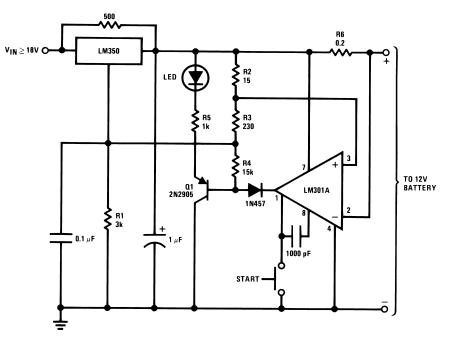


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 $^*0.4 \leq \mathsf{R}_1 \leq 120 \Omega$



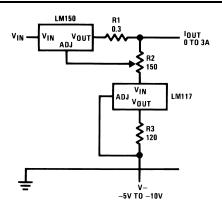
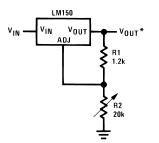


Figure 31. Adjustable Current Regulator

*Minimum output current ≈ 4 mA





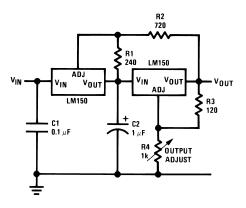
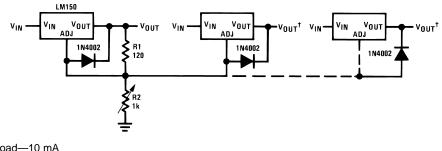


Figure 35. Tracking Preregulator



†Minimum load—10 mA *All outputs within ±100 mV



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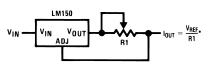


Figure 32. Precision Current Limiter

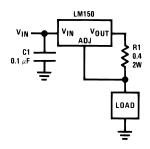


Figure 34. 3A Current Regulator

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*R_S—sets output impedance of charger: $Z_{OUT} = R_S \left(1 + \frac{R_2}{R_1}\right)$

Use of R_S allows low charging rates with fully charged battery. **1000 μF is recommended to filter out any input transients

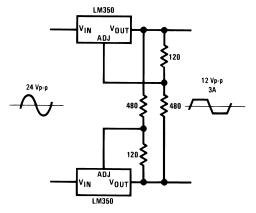


Figure 37. AC Voltage Regulator

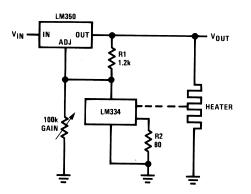


Figure 39. Temperature Controller

*Sets peak current (2A for 0.3Ω)

**1000 μ F is recommended to filter out any input transients.

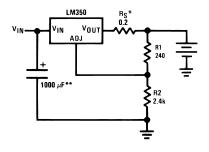


Figure 38. Simple 12V Battery Charger

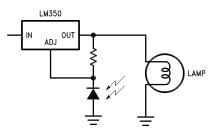
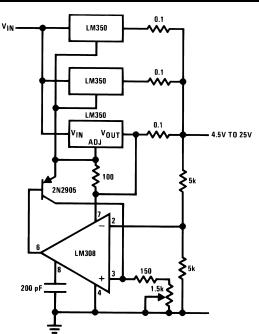
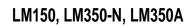


Figure 40. Light Controller







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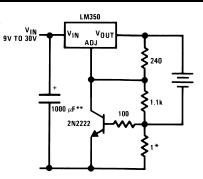
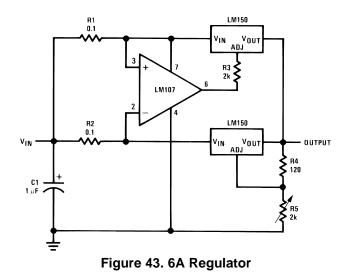




Figure 42. Current Limited 6V Charger



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REVISION HISTORY

| Changes from Revision A (March 2013) to Revision B | | | | | | |
|--|--|------|--|--|--|--|
| • | Changed layout of National Data Sheet to TI format | . 15 | | | | |



31-Oct-2014

PACKAGING INFORMATION

| Orderable Device | Status | Package Type | • | Pins | 0 | Eco Plan | Lead/Ball Finish | MSL Peak Temp | Op Temp (°C) | Device Marking | Samples |
|-------------------|--------|--------------|---------|------|-----|----------------------------|----------------------|------------------|--------------|-------------------|---------|
| | (1) | | Drawing | | Qty | (2) | (6) | (3) | | (4/5) | |
| LM150G MD8 | ACTIVE | DIESALE | Y | 0 | 100 | Green (RoHS & no Sb/Br) | Call TI | Level-1-NA-UNLIM | -55 to 125 | | Samples |
| LM350AT | NRND | TO-220 | NDE | 3 | 45 | TBD | Call TI | Call TI | -40 to 125 | LM350AT P+ | |
| LM350AT/NOPB | ACTIVE | TO-220 | NDE | 3 | 45 | Green (RoHS & no Sb/Br) | CU SN | Level-1-NA-UNLIM | -40 to 125 | LM350AT P+ | Samples |
| LM350K STEEL | ACTIVE | TO-3 | NDS | 2 | 50 | TBD | Call TI | Call TI | 0 to 125 | LM350K STEELP+ | Samples |
| LM350K STEEL/NOPB | ACTIVE | TO-3 | NDS | 2 | 50 | Green (RoHS & no Sb/Br) | Call TI POST-PLATE | Level-1-NA-UNLIM | 0 to 125 | LM350K STEELP+ | Samples |
| LM350T | NRND | TO-220 | NDE | 3 | 45 | TBD | Call TI | Call TI | 0 to 125 | LM350T P+ | |
| LM350T/NOPB | ACTIVE | TO-220 | NDE | 3 | 45 | Green (RoHS & no Sb/Br) | CU SN | Level-1-NA-UNLIM | 0 to 125 | LM350T P+ | Samples |

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.



PACKAGE OPTION ADDENDUM

31-Oct-2014

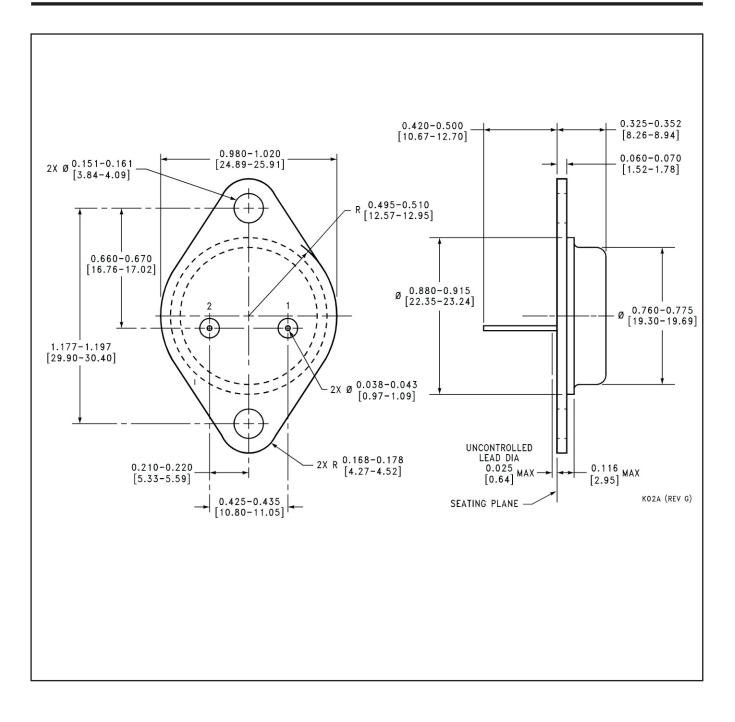
⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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NDS0002A





MECHANICAL DATA

NDE0003B





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