



# Obsolescence Notice

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**Features**

- Complete primary rate 2048 kbit/s E1 line driver and receiver with clock recovery
- Meets ETSI requirements [ETSI ETS 300 011, NET 5]
- Built-in selectable termination impedance (120 Ω/75 Ω)
- Onboard pulse transformers for transmit and receive
- No external crystal required for clock recovery
- Loss of signal indication
- Compatible with MT8979, MT9079 and other E1 Framers
- Single +5 V operation

**Applications**

- Primary rate ISDN network Interfaces
- Multiplexer equipment
- E1 Digital Loop Carrier (DLC) equipment
- Digital Cross-connect Systems (DCS)

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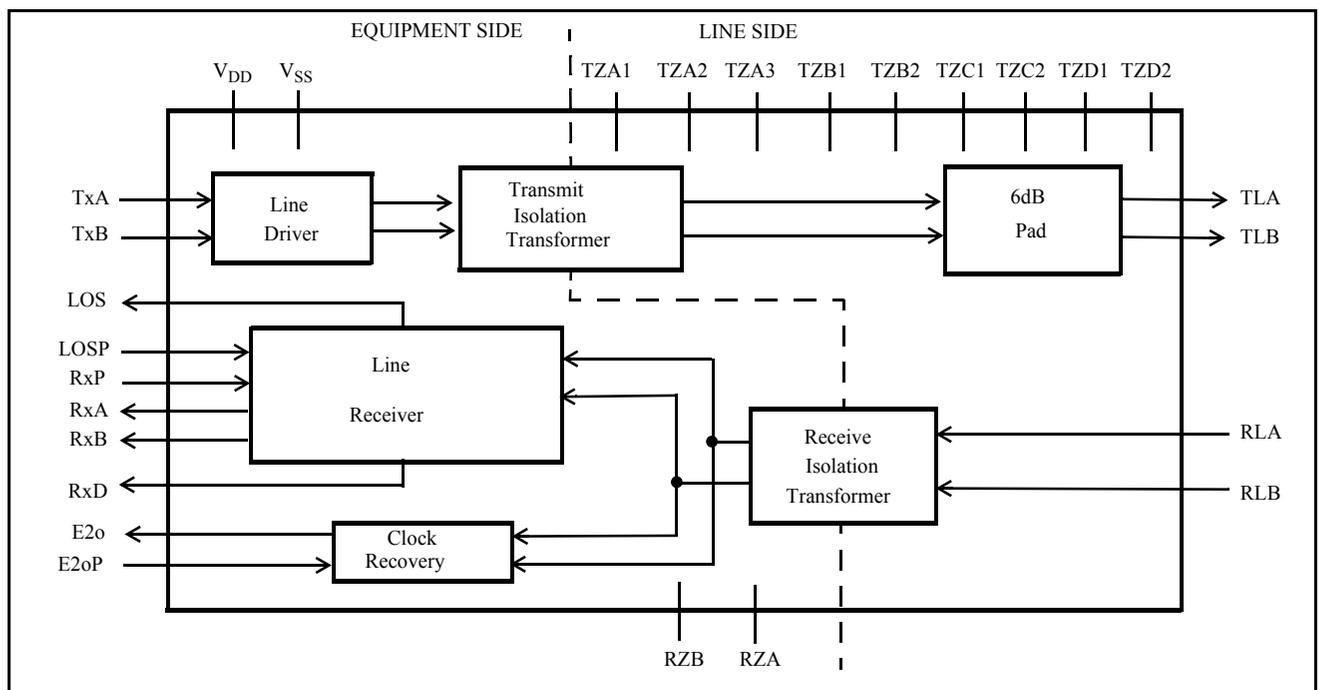
**Ordering Information**

MH897931	28 Pin DIL*	Boxed
*Pb Free Matte Tin		
0°C to +70°C		

**Description**

The Zarlink MH89793 is an E1 Line Interface Unit (LIU) designed to meet the requirements of G.703 2048 kbits/s transmission. It incorporates all of the analog front-end components necessary to realize a complete, fully compliant short-haul E1 analog termination. These include, clock extractor, line driver/receiver, impedance matching resistors, and line transformers. The device is suitable for both 120 Ω and 75 Ω applications, by external hardware control. No external components, such as crystals, inductors or transformers are required. An external clock reference is also not required.

The MH89793 is compatible with the Zarlink MT8979 and MT9079 E1 digital framers, as well as other commercially available E1 framers. The MH89793 requires only a single 5 volt supply.


**Figure 1 - Functional Block Diagram**



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