

Two selectable differential input channels  
On-chip active low noise PGA with selectable gain of 3264 and 128  
On-chip power supply regulator for load-cell and ADC analog power supply  
On-chip oscillator requiring no external component with optional external crystal  
On-chip power-on-reset  
Simple digital control and serial interface: pin-driven controls no programming needed  
Selectable 10SPS or 80SPS output data rate  
Simultaneous 50 and 60Hz supply rejection  
Current consumption including on-chip analog power supply regulator: normal operation < 1.5mA power down < 1uA  
Operation supply voltage range: 2.6 ~ 5.5V  
Operating Temperature Range: -20 degree ~ +85 degree

Connection:

Red to E+

Black to E-

Green to A+

White to A-

### Technical Parameters

Range: 1/5/10/20kg

Output sensitivity:  $1.0 \pm 0.1 \text{ mV/V}$

Zero output:  $\pm 0.1 \text{ mV/V}$

Nonlinear: 0.05% F.S

Lag: 0.05% F.S

Repeatability: 0.05% F.S

Creep: 0.05% F.S/3min

Output impedance:  $1000 \pm 5\%$  ohms

Zero temperature drift: 0.05% F.S/10°C

Temperature sensitivity drift: 0.05% F.S/10°C