



Winstar Display Co., LTD 華凌光電股份有限公司



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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WG12864E-WGH-TZ

<p>APPROVED BY:</p> <p>(FOR CUSTOMER USE ONLY)</p>	
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
ISSUED DATE:			



MODLE NO :

DOC. FIRST ISSUE

RECORDS OF REVISION

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2004.02.18		First issue

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1. Module Classification Information

W G 1 2 8 6 4 E - W G H - T Z

• , *f* „ ... † ‡ ^

• Brand : WINSTAR DISPLAY CORPORATION

, Display Type : H→ Character Type, G→ Graphic Type

f Display Font : 128 * 64 Dots + 4 Icon

„ Model serials no.

... Backlight Type : N→ Without backlight

B→ EL, Blue green

A→ LED, Amber

D→ EL, Green

R→ LED, Red

W→ EL, White

O→ LED, Orange

F→ CCFL, White

G→ LED, Green

Y→ LED, Yellow Green

† LCD Mode : B→ TN Positive, Gray

T→ FSTN Negative

N→ TN Negative,

G→ STN Positive, Gray

Y→ STN Positive, Yellow Green

M→ STN Negative, Blue

F→ FSTN Positive

‡ LCD Polarizer Type/ A→ Reflective, N.T, 6:00

H→ Transflective, W.T,6:00

Temperature range/

D→ Reflective, N.T, 12:00

K→ Transflective, W.T,12:00

View direction

G→ Reflective, W. T, 6:00

C→ Transmissive, N.T,6:00

J→ Reflective, W. T, 12:00

F→ Transmissive, N.T,12:00

B→ Transflective, N.T,6:00

I→ Transmissive, W. T, 6:00

E→ Transflective, N.T.12:00

L→ Transmissive, W.T,12:00

^ Special Code

T : Build in Negative Voltage and temperature compensation ;

2. Precautions in Use of LCD Module

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD Module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.

3. General Specification

ITEM	STANDARD VALUE	UNIT
Number of dots	128 ×64 + 4 Icon	dots
Module dimension	54.0(W) ×50.0(H) ×7.5 max(T)	mm
View area	43.5(W) ×29.0(H)	mm
Active area	40.92(W) ×26.92(H)	mm
Dot size	0.28(W) ×0.35(H)	mm
Dot pitch	0.32(W) ×0.39(H)	mm
LCD type	STN, positive, transfective, gray	
View direction	6 o'clock	
Backlight	EL (white)	

4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNNIT
Operating Temperature	T_{OP}	-20	-	+70	°C
Storage Temperature	T_{ST}	-30	-	+80	°C
Input Voltage	V_I	0	-	V_{DD}	V
Supply Voltage For Logic	V_{DD}	0	-	6.7	V
Supply Voltage For LCD	$V_{DD}-V_{LCD}$	0	-	17	V

5. Electrical Characteristics

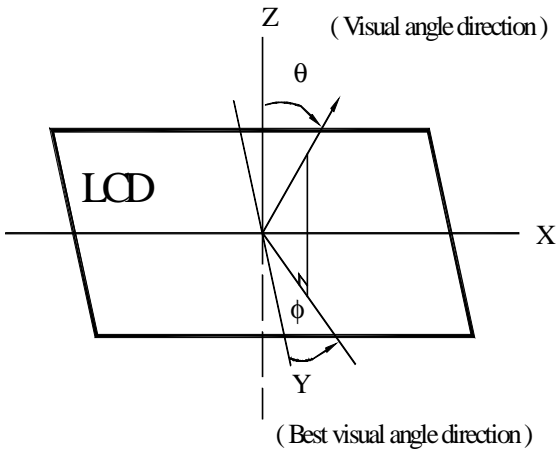
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-	2.8	5.0	5.5	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=-20^{\circ}\text{C}$	-	-	10.0	V
		$T_a=25^{\circ}\text{C}$	-	8.5	-	V
		$T_a=+70^{\circ}\text{C}$	7.0	-	-	V
Input High Vol	V_{IH}	-	$0.7V_{DD}$	-	V_{DD}	V
Input Low Vol	V_{IL}	-	0	-	$0.3V_{DD}$	V
Output High Vol	V_{OH}	-	2.4	-	-	V
Output Low Vol.	V_{OL}	-	-	-	0.4	V
Power Supply Current	I_{DD} (EL OFF)		-	10.0	-	mA
Power Supply Current	I_{DD} (EL ON)		-	40.0	-	mA

6. Optical Characteristics

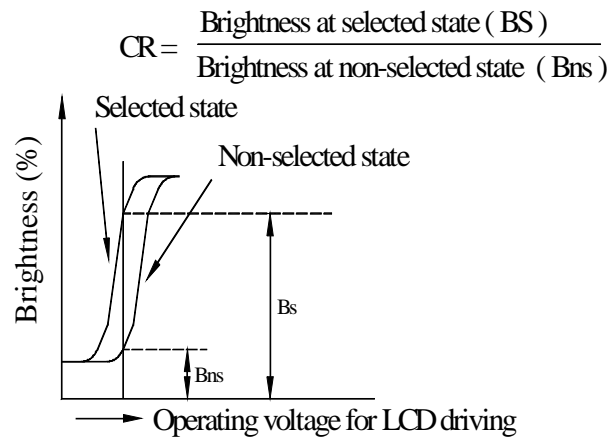
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
View Angle	(V) θ	$CR \geq 2$	20		40	deg.
	(H) ϕ	$CR \geq 2$	-30		30	deg.
Contrast Ratio	CR	-	2	3		-
Response Time	T rise	-		135	270	ms
	T fall	-		265	400	ms

6.1 Definitions

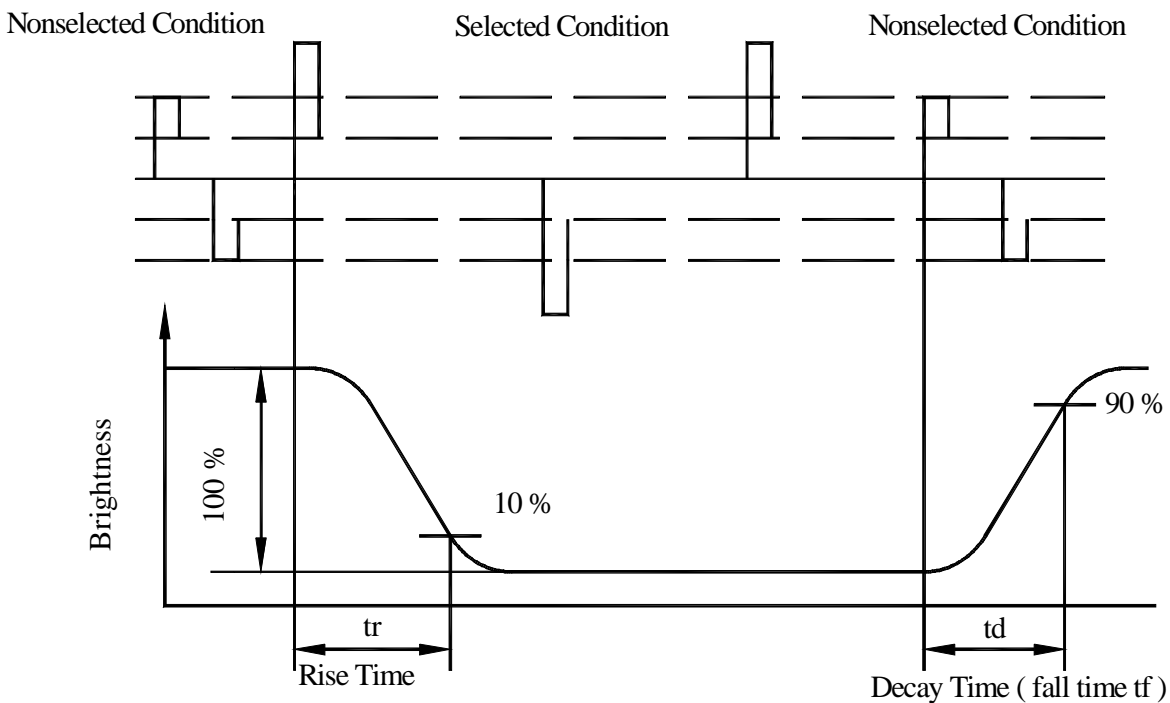
■ View Angles



■ Contrast Ratio



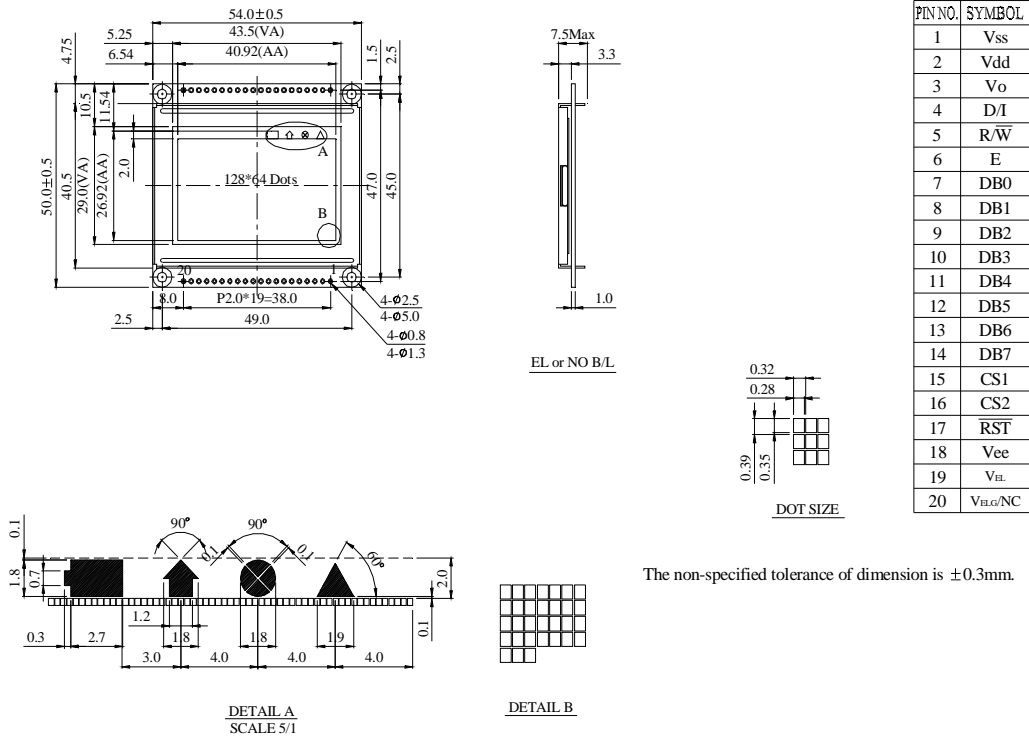
■ Response time



7.Interface Pin Function

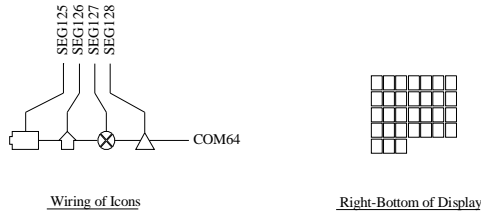
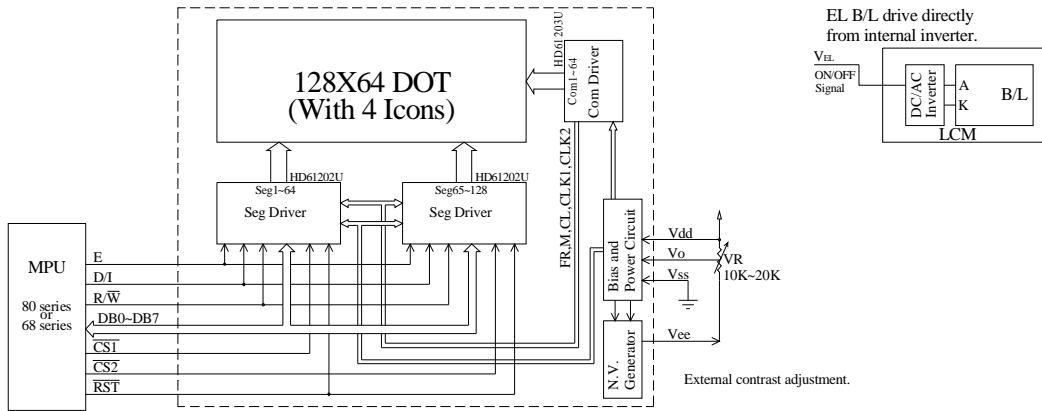
Pin No.	Symbol	Level	Description
1	GND	0V	Ground
2	V _{DD}	2.8~5.5V	Supply voltage for logic
3	V _O	(Variable)	Operating voltage for LCD
4	D/I	H/L	H: Data , L: Instruction
5	R/W	H/L	H: Read(MPU← Module) , L :Write(MPU→ Module)
6	E	H	Enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	CS1	H	Chip Enable (Select Column 1 ~ Column 64)
16	CS2	H	Chip Enable (Select Column 65 ~ Column 128)
17	RST	L	Reset signal
18	V _{EE}		Negative Voltage
19	BL+	-	Enable (on/off) for EL B/L
20	BL-	-	No connection

8. Contour Drawing & Block Diagram



PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	D/I
5	R/W
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	CS1
16	CS2
17	RST
18	Vee
19	Vii
20	Vii,G/NC

The non-specified tolerance of dimension is ±0.3mm.



9. Function Description

Display On/Off

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the screen with D = 0, it remains in the display data RAM. Therefore, you can make it appear by changing D = 0 into D = 1.

Display Start Line

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	A	A	A	A	A	A

Z address AAAAAA (binary) of the display data RAM is set in the display start line register and displayed at the top of the screen. Figure 2. shows examples of display (1/64 duty cycle) when the start line = 0-3. When the display duty cycle is 1/64 or more (ex. 1/32, 1/24 etc.), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

Set Page (X Address)

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	A	A	A

X address AAA (binary) of the display data RAM is set in the X address register. After that, writing or reading to or from MPU is executed in this specified page until the next page is set. See Figure 1.

Set Y Address

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	A	A	A	A	A	A

Y address AAAAAA (binary) of the display data RAM is set in the Y address counter. After that, Y address counter is increased by 1 every time the data is written or read to or from MPU.

Status Read

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	Busy	0	On/Off	RESET	0	0	0	0

· Busy

When busy is 1, the LSI is executing internal operations. No instruction are accepted while busy is 1, so you should make sure that busy is 0 before writing the next instruction.

· ON/OFF

Shows the liquid crystal display condition: on condition or off condition.

When on/off is 1, the display is in off condition.

When on/off is 0, the display is in on condition.

· RESET

RESET = 1 shows that the system system is being initialized. In this condition, no instructions

except status read can be accepted.

RESET = 0 shows that initializing has system is in the usual operation condition.

Write Display Data

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	D	D	D	D	D	D	D	D

Writes 8-bit data DDDDDDDD (binary) into the display data RAM. The Y address is increased by 1 automatically.

Read Display Data

R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D	D	D	D	1	D	D	D

Reads out 8-bit data DDDDDDDD (binary) from the display data RAM. Then Y address is increased by 1 automatically.

One dummy read is necessary right after the address setting. For details, refer to the explanation of output register in “Function of Each Block”.

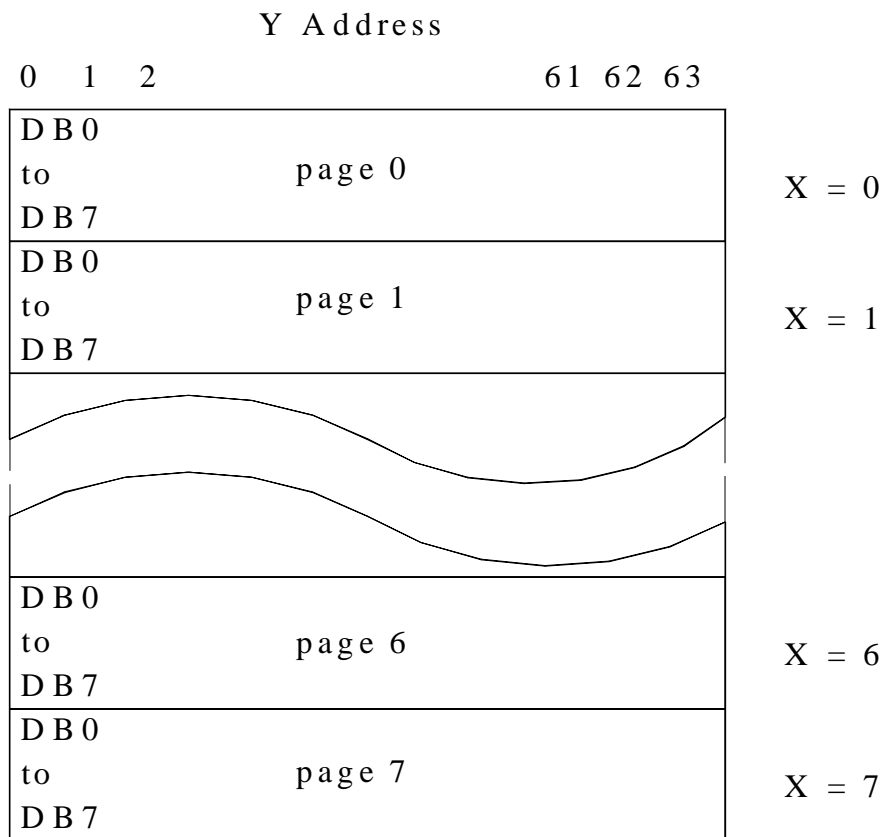
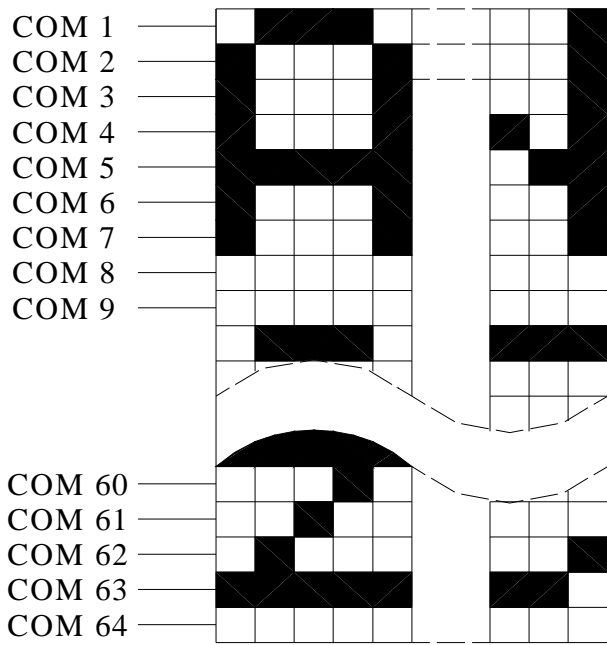
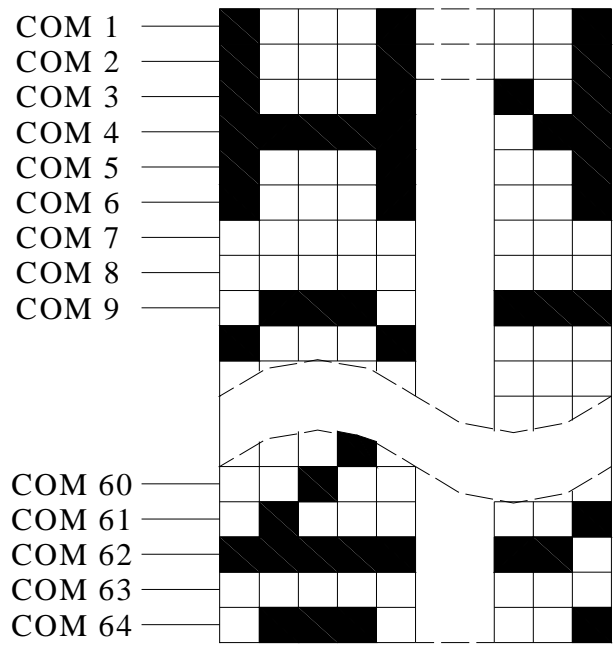


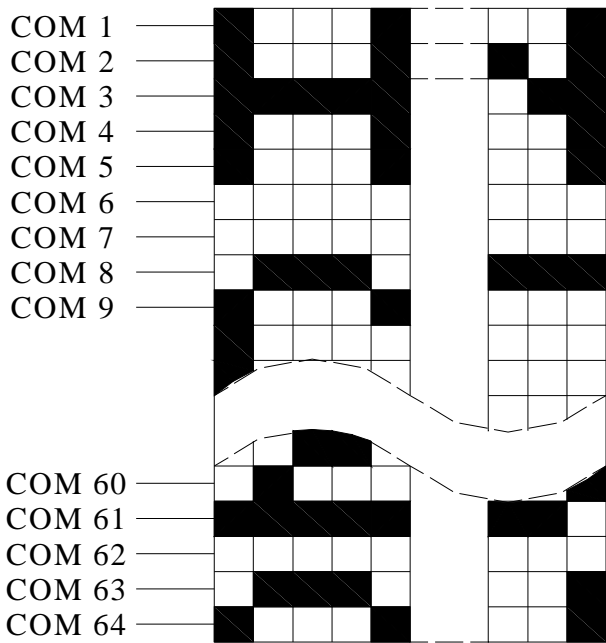
Figure 1.



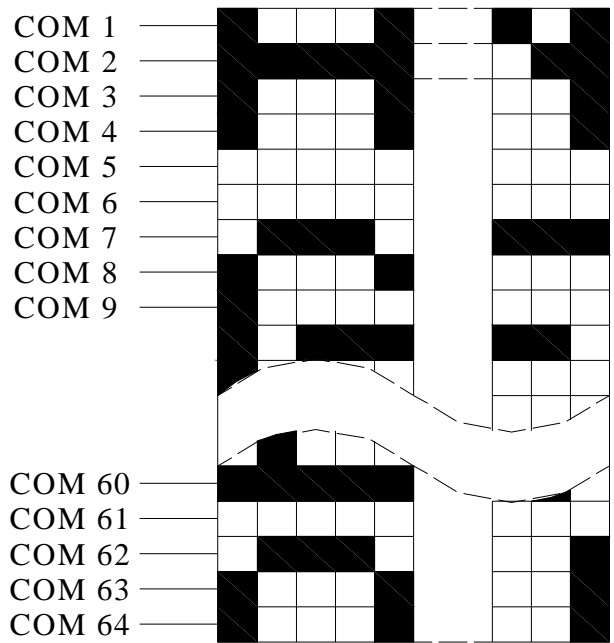
Start line = 0



Start line = 1



Start line = 3



Start line = 4

Figure 2.

10. Instruction Table

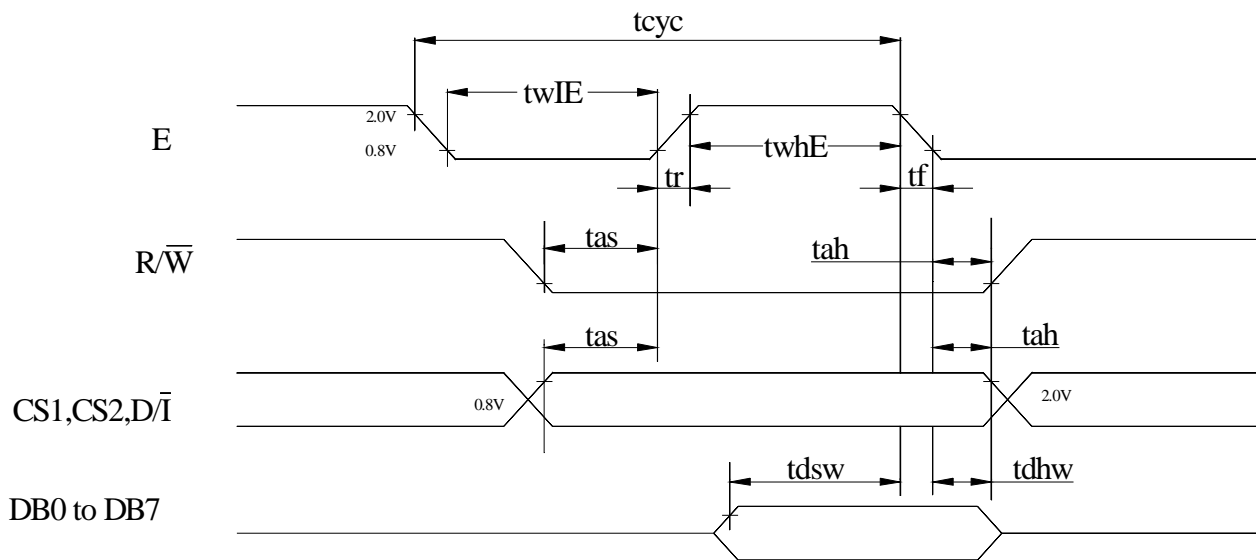
The display control instructions control the internal state of the KS0108B. Instruction is received from MPU to KS0108B for the display control. The following table shows various instructions.

Instruction	D/I	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	0	0	0	0	1	1	1	1	1	0/1	Controls the display on or off. Internal status and display RAM data are not affected. 0:OFF, 1:ON
Set Address	0	0	0	1	Y address (0~63)						Sets the Y address in the Y address counter.
Set Page (X address)	0	0	1	0	1	1	1	Page (0 ~7)			Sets the X address at the X address register.
Display Start Line	0	0	1	1	Display start line(0~63)						Indicates the display data RAM displayed at the top of the screen.
Status Read	0	1	B U S Y	0	ON/ OFF	R E S E T	0	0	0	0	Read status. BUSY 0:Ready 1:In operation ON/OFF 0:Display ON 1:Display OFF RESET 0:Normal 1:Reset
Write Display Data	1	0	Display Data								Writes data (DB0:7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read Display Data	1	1	Display Data								Reads data (DB0:7) from display data RAM to the data bus.

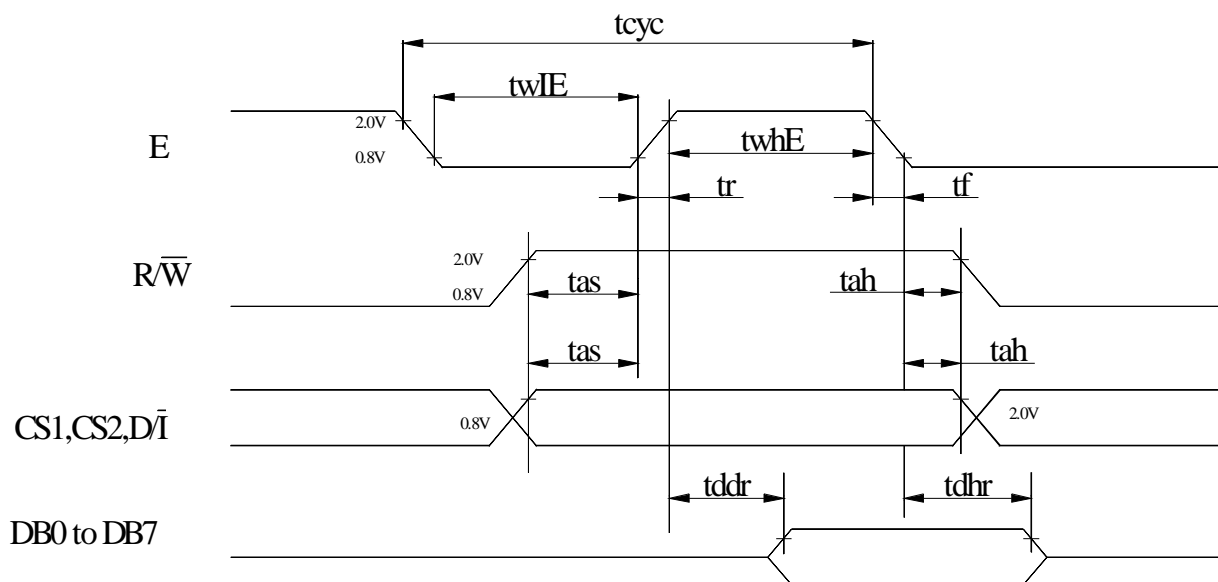
11. Timing Characteristics

MPU Interface

Characteristic	Symbol	Min	Typ	Max	Unit
E cycle	tcyc	1000	-	-	ns
E high level width	twhE	450	-	-	ns
E low level width	twlE	450	-	-	ns
E rise time	tr	-	-	25	ns
E fall time	tf	-	-	25	ns
Address set-up time	tas	140	-	-	ns
Address hold time	tah	10	-	-	ns
Data set-up time	tdsw	200	-	-	ns
Data delay time	tddr	-	-	320	ns
Data hold time (write)	tdhw	10	-	-	ns
Data hold time (read)	tdhr	20	-	-	ns



MPU Write Timing



MPU Read Timing

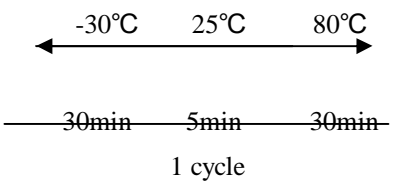
12. Quality Assurance

◆ Screen Cosmetic Criteria

No.	Defect	Judgement Criterion	Partition																				
1	Spots	<p>A)Clear</p> <table border="1"> <thead> <tr> <th>Size:d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>$d \leq 0.1$</td> <td>Disregard</td> </tr> <tr> <td>$0.1 < d \leq 0.2$</td> <td>6</td> </tr> <tr> <td>$0.2 < d \leq 0.3$</td> <td>2</td> </tr> <tr> <td>$0.3 < d$</td> <td>0</td> </tr> </tbody> </table> <p>Note:Including pin holes and defective dots which must be within one pixel size.</p> <p>B)Unclear</p> <table border="1"> <thead> <tr> <th>Size:d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>$d \leq 0.2$</td> <td>Disregard</td> </tr> <tr> <td>$0.2 < d \leq 0.5$</td> <td>6</td> </tr> <tr> <td>$0.5 < d \leq 0.7$</td> <td>2</td> </tr> <tr> <td>$0.7 < d$</td> <td>0</td> </tr> </tbody> </table>	Size:d mm	Acceptable Qty in active area	$d \leq 0.1$	Disregard	$0.1 < d \leq 0.2$	6	$0.2 < d \leq 0.3$	2	$0.3 < d$	0	Size:d mm	Acceptable Qty in active area	$d \leq 0.2$	Disregard	$0.2 < d \leq 0.5$	6	$0.5 < d \leq 0.7$	2	$0.7 < d$	0	Minor
Size:d mm	Acceptable Qty in active area																						
$d \leq 0.1$	Disregard																						
$0.1 < d \leq 0.2$	6																						
$0.2 < d \leq 0.3$	2																						
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$d \leq 0.2$	Disregard																						
$0.2 < d \leq 0.5$	6																						
$0.5 < d \leq 0.7$	2																						
$0.7 < d$	0																						
2	Bubbles in Polarizer	<table border="1"> <thead> <tr> <th>Size:d mm</th> <th>Acceptable Qty in active area</th> </tr> </thead> <tbody> <tr> <td>$d \leq 0.3$</td> <td>Disregard</td> </tr> <tr> <td>$0.3 < d \leq 1.0$</td> <td>3</td> </tr> <tr> <td>$1.0 < d \leq 1.5$</td> <td>1</td> </tr> <tr> <td>$1.5 < d$</td> <td>0</td> </tr> </tbody> </table>	Size:d mm	Acceptable Qty in active area	$d \leq 0.3$	Disregard	$0.3 < d \leq 1.0$	3	$1.0 < d \leq 1.5$	1	$1.5 < d$	0	Minor										
Size:d mm	Acceptable Qty in active area																						
$d \leq 0.3$	Disregard																						
$0.3 < d \leq 1.0$	3																						
$1.0 < d \leq 1.5$	1																						
$1.5 < d$	0																						
3	Scratch	In accordance with spots cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor																				
4	Allowable Density	Above defects should be separated more than 30mm each other.	Minor																				
5	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-light type should be judged with back-light on state only.	Minor																				

13. RELIABILITY

■ Content of Reliability Test

Environmental Test				
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	—
2	Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	—
3	High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	—
5	High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	80°C,90%RH 96hrs	—
6	High Temperature/ Humidity Operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	70°C,90%RH 96hrs	—
7	Temperature Cycle	Endurance test applying the low and high temperature cycle. 	-30°C/80°C 10 cycles	—
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→ 1.5mmp-p 22~500Hz→ 1.5G Total 0.5hrs	—
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msdc 3 times of each direction	—
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	—
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	—

***Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25°C

14. Backlight Information

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Drive Voltage	Vmax	75	—	85	Vrms	25□C
Drive Wave	Fmax	300	—	1000	Hz	25□C
Brightness		25	-	-	cd/m ²	75~85Vrms/300~400Hz
Power Consumption		-	31.5	-	mW	75~85Vrms/300~400Hz
Chromatism	X	-	0.330	-	-	75~85Vrms/300~400Hz
	Y	-	0.365	-	-	75~85Vrms/300~400Hz
Life time		5000			hour	75~85Vrms/300~400Hz
Color		White			-	-